

30A 100V Schottky Barrier Diode

1 Description

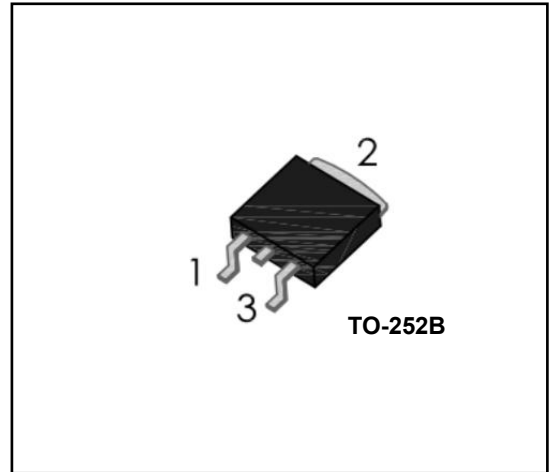
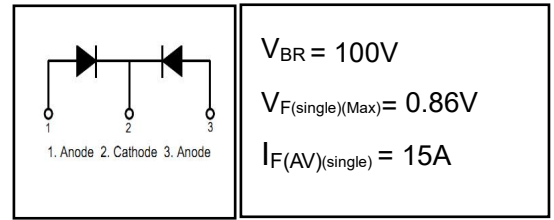
This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150°C junction temperature.

2 Features

- High junction temperature capability
- Low leakage current
- Low thermal resistance
- High frequency operation
- Avalanche specification

3 Applications

- Switching Power Supply
- Power Switching Circuits
- General Purpose



4 Electrical Characteristics

4.1 Absolute Maximum Ratings (Tc=25°C, unless otherwise noted)

PARAMETER		SYMBOL	VALUE	UNIT
Peak Repetitive Reverse Voltage		V_{RRM}	100	V
RMS Reverse Voltage		$V_{R(RMS)}$	80	V
DC Blocking Voltage		V_R	100	V
Average Rectified Forward Current(single)	Tc=120°C	$I_{F(AV)}$	15	A
Average Rectified Forward Current(double)			30	A
Repetitive Peak Surge Current(single)		I_{FRM}	20	A
Nonrepetitive Peak Surge Current(single)	tp=8.3ms	I_{FSM}	200	A
Avalanche Energy(single)	L=1mH	E_{AS}	32	mJ
Operating Junction Temperature Range		T_j	-55~150	°C
Storage Temperature Range		T_{stg}	-55~150	°C
ESD (Machine Model=C)			>400	V
ESD (Human Body Model=3B)			>8000	V
ESD(Noncontact)			>15000	V

4.2 Thermal Characteristics

PARAMETER	SYMBOL	VALUE	UNIT
Thermal Resistance, Junction to Case-sink	R_{thJC}	1.5	°C/W

4.3 Electrical Characteristics (Tc=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Maximum Instantaneous Forward Voltage	V _F	I _F = 10A	-	0.79	-	V
		I _F = 15A	-	0.83	0.86	V
		I _F = 15A, T _a = 125°C	-	0.75	-	V
		I _F = 30A	-	0.91	1.1	V
Maximum Instantaneous Reverse	I _R	V _R = 100V	-	2.6	20	μA
		V _R = 100V, T _a = 125°C	-	-	20	mA
Total capacitance	C _{tot}	V _R =4V f=1MHz	-	209	-	pF
DC Blocking Voltage	V _{BR}	I _R =100μA	100	120	-	V

DEFINITIONS

V_F = Instantaneous forward voltage (pw = 300μs, D = 2%).

I_R = Instantaneous reverse current.

RθJC = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

5 Typical characteristics diagrams

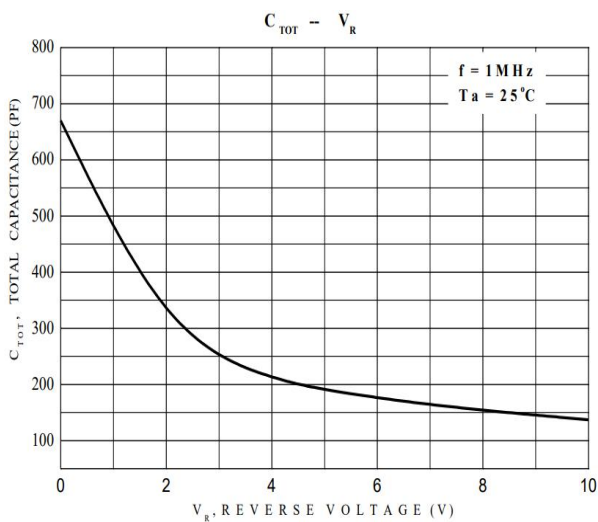


FIGURE 1. Total capacitance vs Voltage

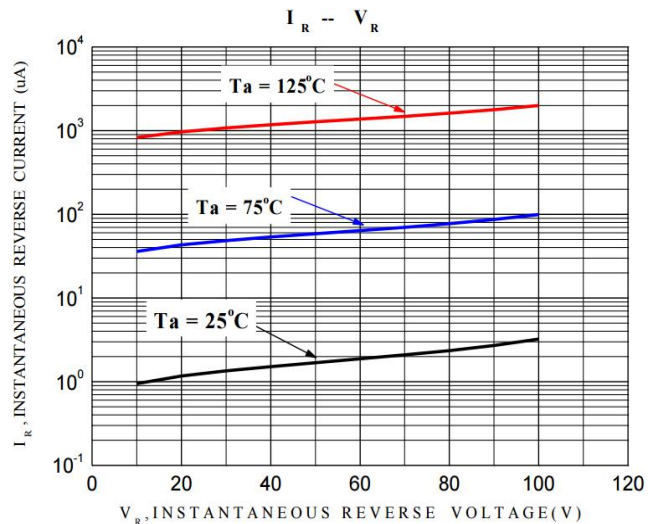


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

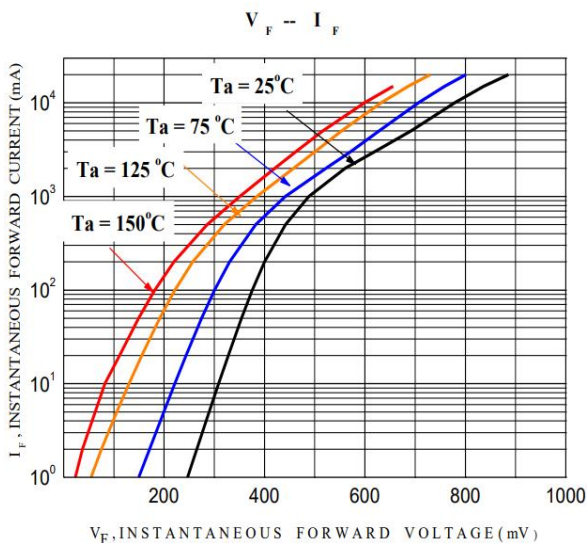


FIGURE 3. FORWARD CURRENT vs FORWARD VOLTAGE

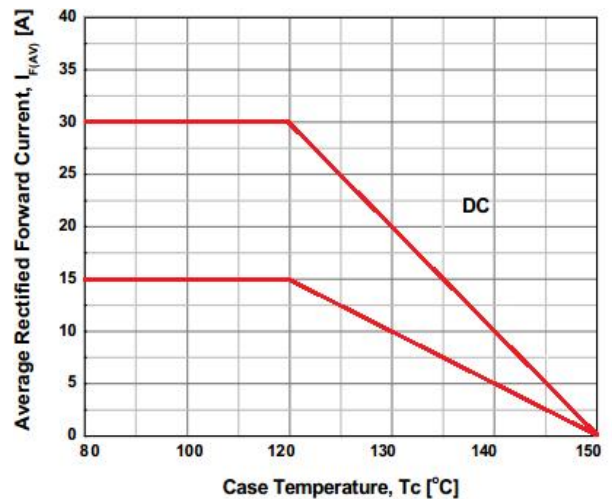


FIGURE 4. CURRENT DERATING CURVE

6 Typical Test Circuit and Waveform

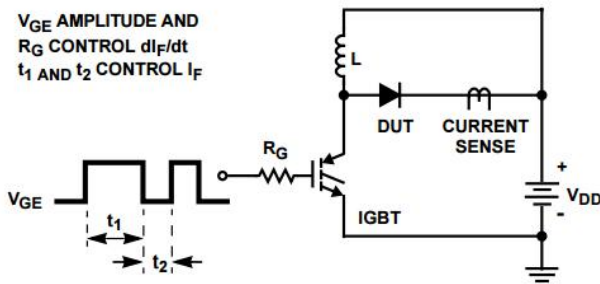


FIGURE 5. trr TEST CIRCUIT

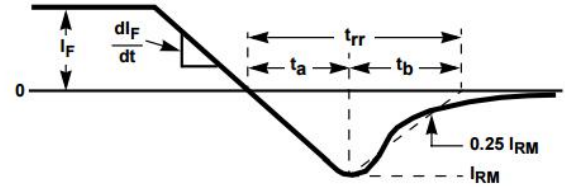


FIGURE 6. trr WAVEFORMS AND DEFINITIONS

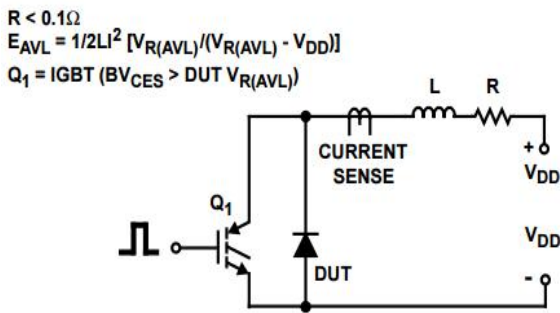


FIGURE 7. AVALANCHE ENERGY TEST CIRCUIT FIGURE

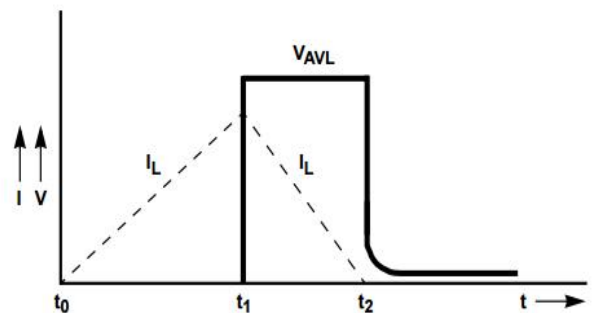


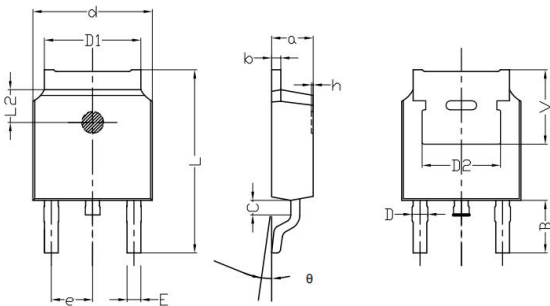
FIGURE 8. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

7 Product Specifications and Packaging Models

Product Model	Package Type	Mark Name	RoHS	Package	Quantity
MBRD30100CT	TO-252B	MBRD30100CT	Pb-free	Braid	2500/disc

8 Dimensions

TO-252 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	min.	max.	min.	max.
a	2.20	2.40	0.087	0.095
b	0.46	0.58	0.018	0.023
c	0.70	0.90	0.028	0.035
D	0.80	0.90	0.032	0.035
d	6.50	6.70	0.2561	0.2640
D1	5.10	5.46	0.201	0.215
D2	4.73	4.93	0.1864	0.1942
A	6.00	6.20	0.2364	0.2443
e	2.19	2.39	0.0861	0.0940
L	10.40	11.00	0.4098	0.4334
B	3.5	3.7	0.1379	0.1458
L2	1.5	1.7	0.0591	0.0670
theta	0	8	0	8
h	0	0.3	0	0.0118
V	5.25	5.45	0.2069	0.2147
E	0.6	0.8	0.0236	0.0315

9 Attentions

- Jiangsu Donghai Semiconductor Technology Co., Ltd. reserves the right to change the specification without prior notice! The customer should obtain the latest version of the information before making the order and verify that the information is complete and up to date.
- It is the responsibility of the purchaser for any failure or failure of any semiconductor product under certain conditions. It is the responsibility of the purchaser to comply with safety standards and to take safety measures in the system design and machine manufacturing of WXDH products in order to avoid potential risk of failure. Injury or property damage.
- Product promotion is endless, our company will be dedicated to provide customers with better products.

10 Appendix

Revision history:

Date	REV.	Description	Page
2023.6.20	1.0	Original	