

100A 40V N-channel Enhancement Mode Power MOSFET

1 Description

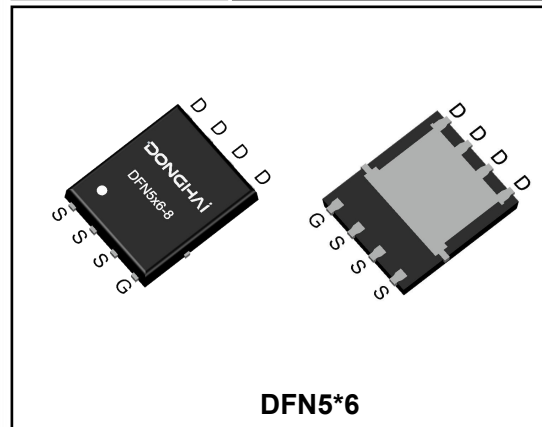
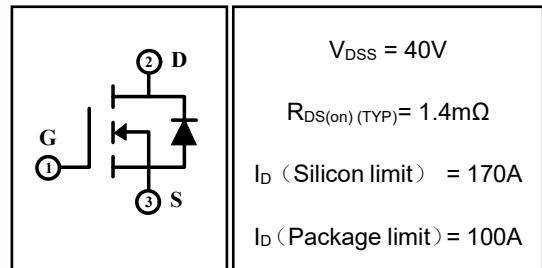
These N-channel enhancement mode power mosfets used advanced split gate trench technology design, provided excellent $R_{DS(on)}$ and low gate charge. Which accords with the RoHS standard.

2 Features

- Low on resistance
- Low gate charge
- Fast switching
- Low reverse transfer capacitances
- 100% single pulse avalanche energy test
- 100% ΔV_{DS} test

3 Applications

- Synchronous rectification in SMPS
- Hard switching and high speed circuit
- Power tools
- UPS
- Motor control



4 Electrical Characteristics

4.1 Absolute Maximum Ratings (Tc=25°C, unless otherwise noted)

Parameter		Symbol	Rating	Units
Drain-to-Source Voltage		V_{DSS}	40	V
Gate-to-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current	$T_C=25^\circ C$ (Silicon limit)	I_D	170	A
	$T_C=25^\circ C$ (Package limit)		100	A
	$T_C=100^\circ C$ (Package limit)		100	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	400	A
Single Pulse Avalanche Energy ⁽⁴⁾		E_{AS}	529	mJ
Power Dissipation	$T_a=25^\circ C$	P_{tot}	3	W
	$T_C=25^\circ C$	P_{tot}	77	W
Junction Temperature Range		T_j	-55~175	$^\circ C$
Storage Temperature Range		T_{stg}	-55~175	$^\circ C$

4.2 Thermal Characteristics

Parameter	Symbol	Value		Units
		Typ	MAX	
Thermal Resistance, Junction to Case-sink	R_{thJC}	1.5	1.95	$^\circ C/W$
Thermal Resistance, Junction to Ambient	R_{thJA}	--	50	$^\circ C/W$

4.3 Electrical Characteristics (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Value			Units
			Min	Typ	Max	
Off Characteristics						
Drain-to-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	40	--	--	V
Drain-to-Source Leakage Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V, T _C =25°C	--	--	1	μA
		V _{DS} =40V, V _{GS} =0V, T _C =125°C	--	--	100	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	--	3	V
Drain-to-Source on-state Resistance	R _{DS(on)}	V _{GS} =10V, I _D =90A	--	1.4	1.8	mΩ
		V _{GS} =4.5V, I _D =90A	--	2.5	3.4	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =20V, f=1.0MHz	--	3296	--	pF
Output Capacitance	C _{oss}		--	1611	--	
Reverse Transfer Capacitance	C _{rss}		--	79	--	
Gate Resisitance	R _G	V _{DD} =0V, V _{GS} =0V, F=1MHz	--	2.3	--	Ω
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	I _D =85A, V _{DD} =20V, V _{GS} =10V, R _{GEN} =1.6Ω	--	12.6	--	nS
Turn-on Rise Time	t _r		--	95.6	--	
Turn-off Delay Time	t _{d(off)}		--	40.8	--	
Turn-off Fall Time	t _f		--	102	--	
Total Gate Charge	Q _g	I _D =90A, V _{DD} =20V, V _{GS} =10V	--	60	--	nC
Gate-to-Source Charge	Q _{gs}		--	17.4	--	
Gate-to-Drain("Miller") Charge	Q _{gd}		--	10.5	--	
Drain-Source Diode Characteristics						
Diode Forward Voltage ⁽³⁾	V _{SD}	V _{GS} =0V, I _S =30A	--	--	1.2	V
Diode Forward Current	I _S		--	--	100	A
Reverse Recovery Time ⁽³⁾	t _{rr}	T _J =25°C, I _F =50A, di _F /dt=100A/μS, V _{GS} =0V	--	33	--	nS
Reverse Recovery Charge ⁽³⁾	Q _{rr}		--	16.4	--	nC

Notes:

- 1: Repetitive rating, pulse width limited by maximum junction temperature.
- 2: Surface mounted on FR4 Board, t≤10sec.
- 3: Pulse width ≤ 300μs, duty cycle ≤ 2%.
- 4: L=0.5mH, V_{DD}=32V, R=25Ω, V_{GATE}=40V, Start T_J=25°C.

5 Typical characteristics diagrams

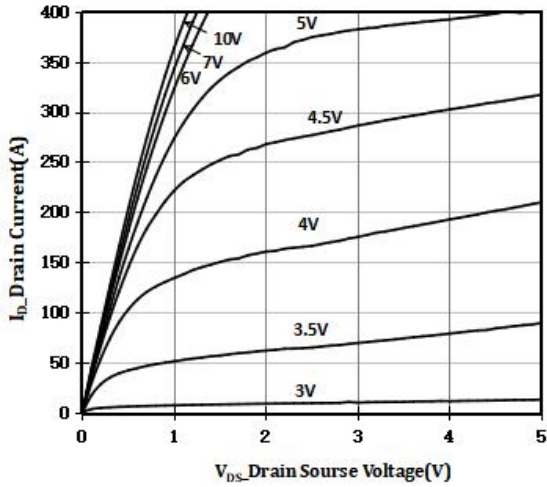


Fig 1. Output Characteristics

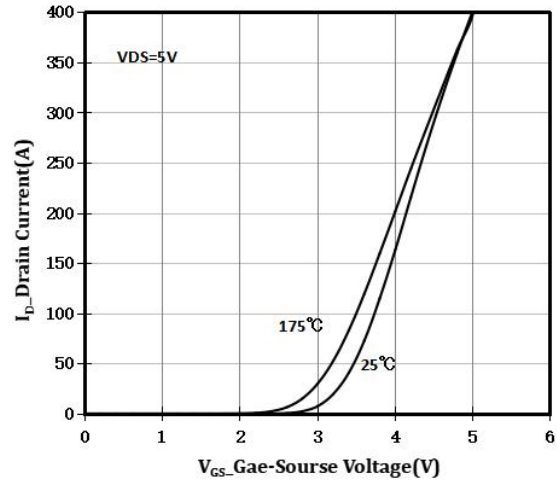


Fig 2. Transfer Characteristics

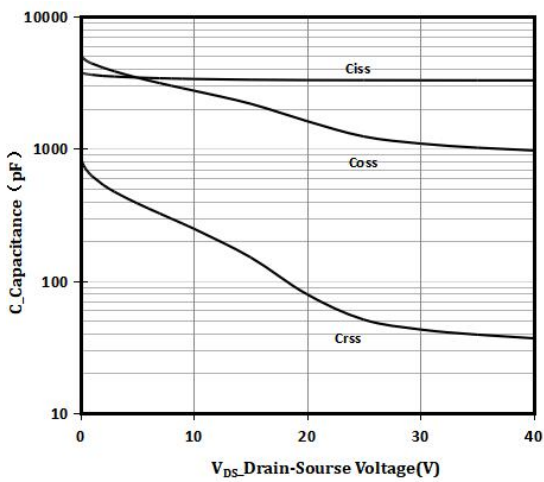


Fig 3. Capacitance vs Vds

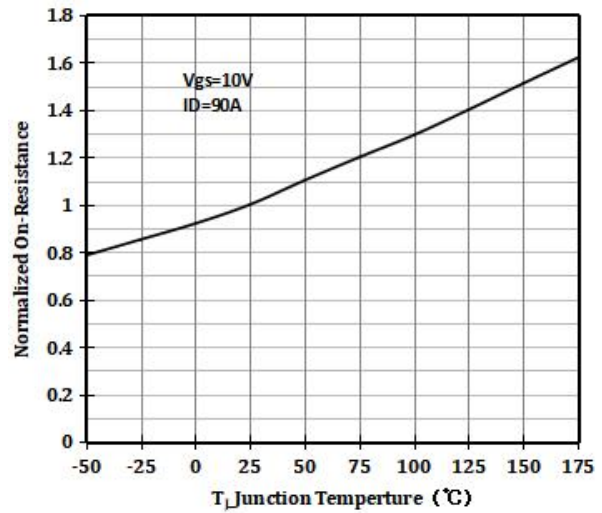


Fig 4. Rds(on)-Junction Temperature

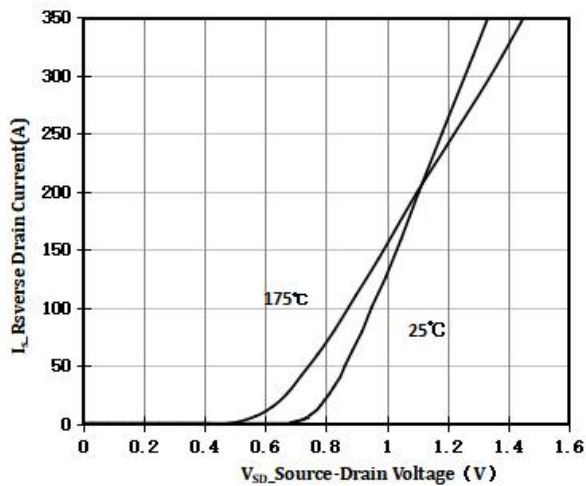


Fig 5. Source-Drain Diode Forward

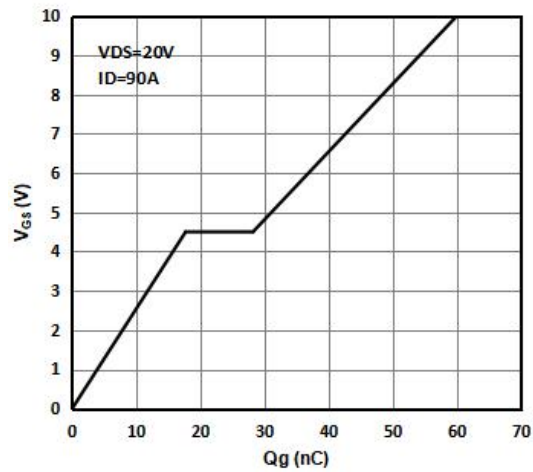


Figure 6 Gate Charge

5 Typical characteristics diagrams(Continues)

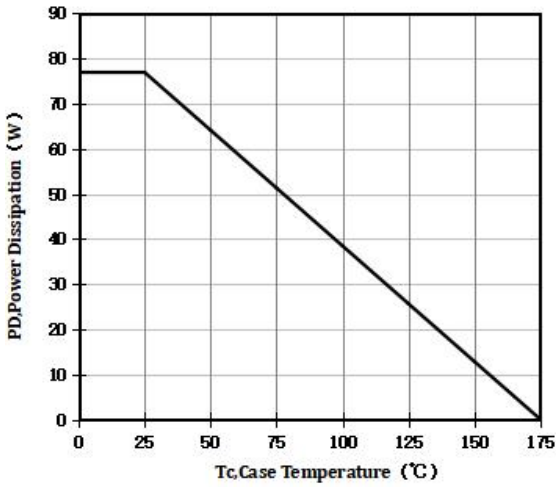


Fig 7. Power De-rating

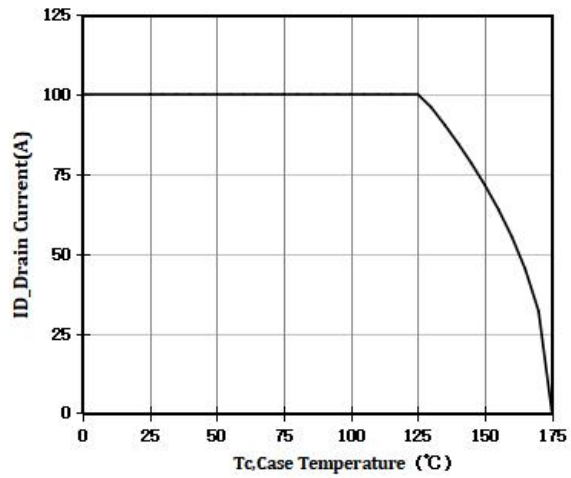


Fig 8. Current De-rating

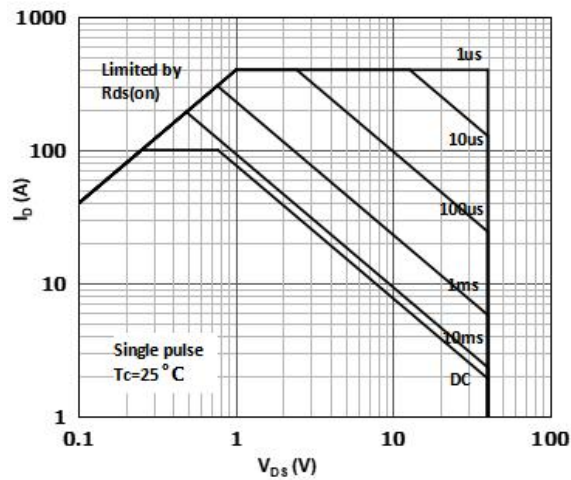


Fig 9. Safe Operation Area

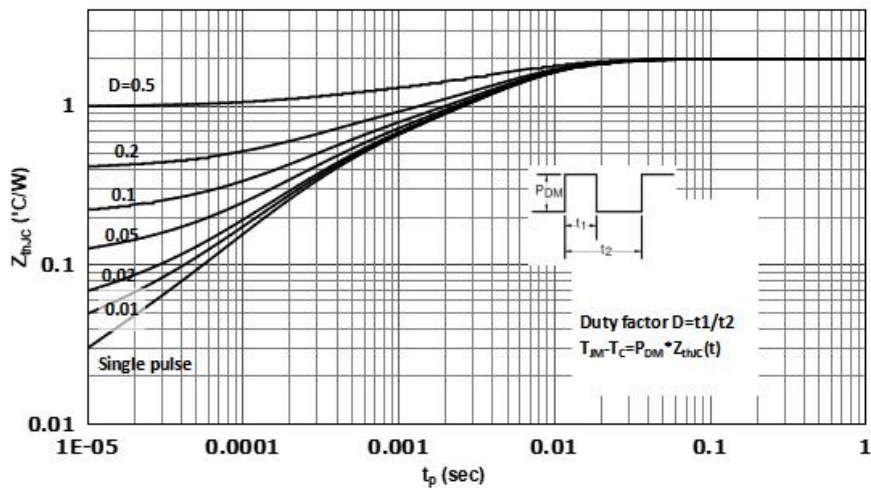
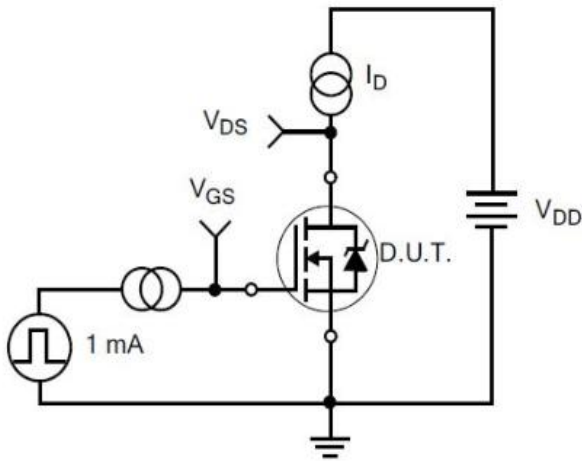
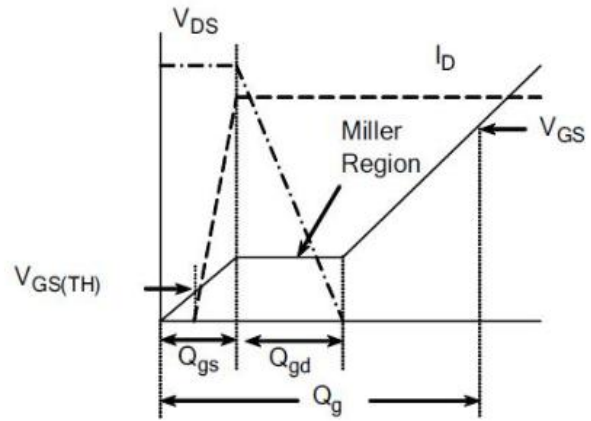


Fig 10. Normalized Maximum Transient Thermal Impedance

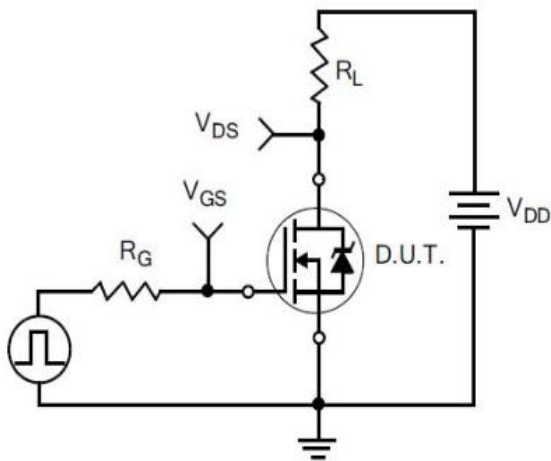
6 Typical Test Circuit and Waveform



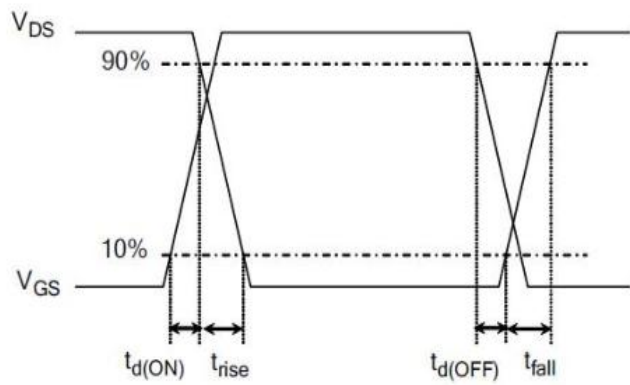
1) Gate Charge Test Circuit



2) Gate Charge Waveform

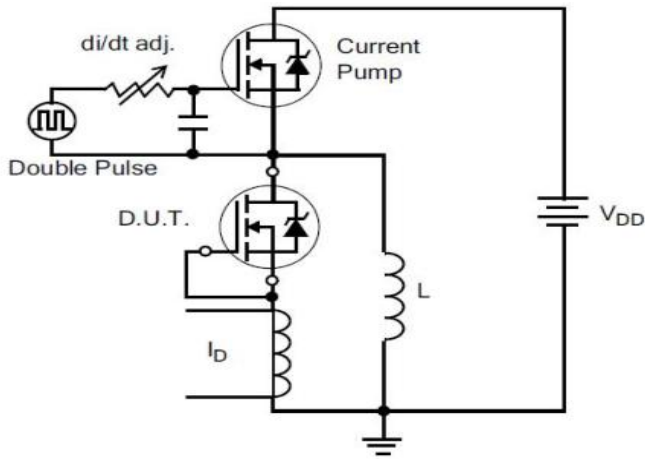


3) Resistive Switching Test Circuit

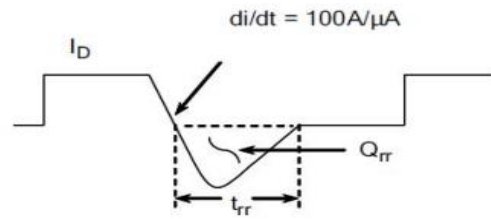


4) Resistive Switching Waveforms

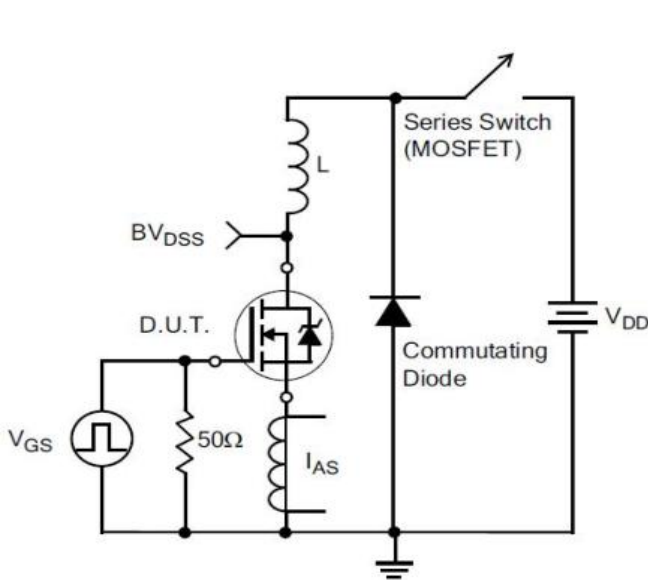
6 Typical Test Circuit and Waveform(continues)



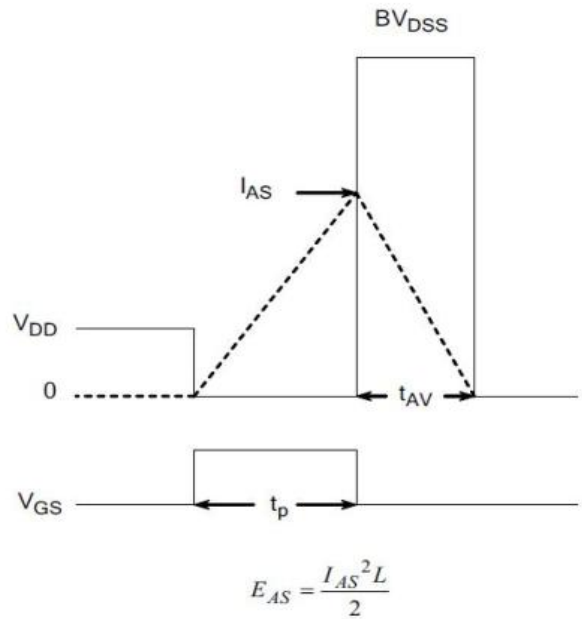
5) Diode Reverse Recovery Test Circuit



6) Diode Reverse Recovery Waveform

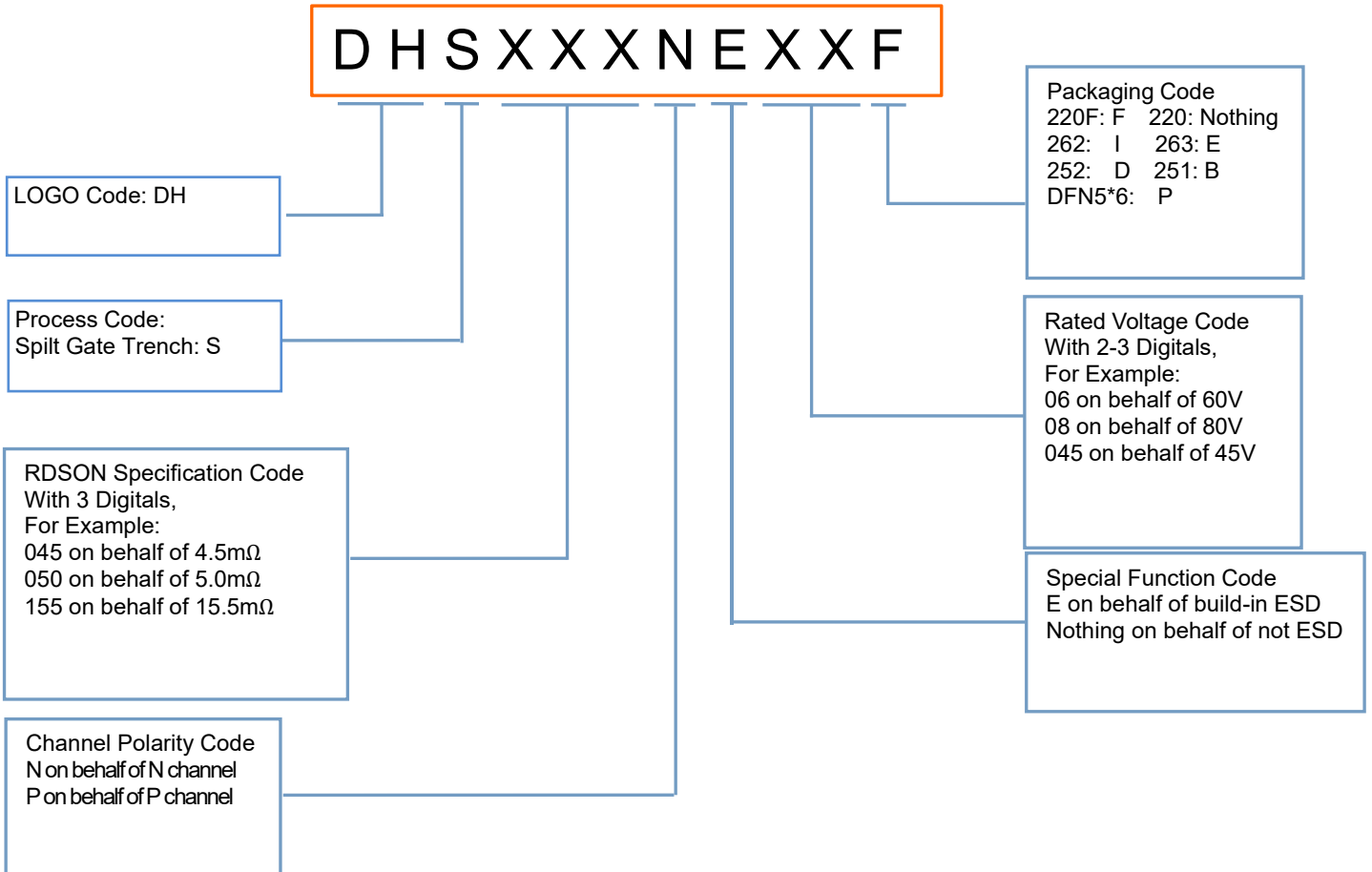


7) . Unclamped Inductive Switching Test Circuit



8) Unclamped Inductive Switching Waveforms

7 Product Names Rules

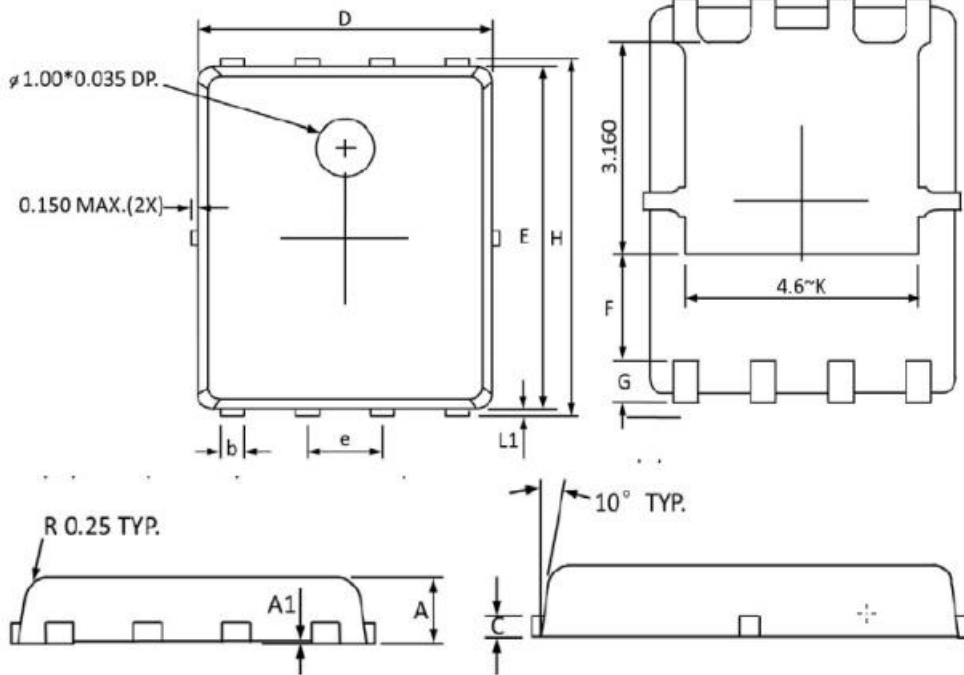


8 Product Specifications and Packaging Models

Product Model	Package Type	Mark Name	RoHS	Package	Quantity
DHS021N04P	DFN5*6-8	DHS021N04P	Pb-free	Tape & Reel	3000/box

9 Dimensions

PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.032	0.039
A1	0.000	0.005	0.000	0.000
b	0.350	0.490	0.014	0.019
C	0.254 Ref		0.254 Ref	
D	4.900	5.100	0.193	0.200
E	5.700	5.900	0.225	0.232
e	1.27 BSC		1.27 BSC	
F	1.600 Ref		1.600 Ref	
G	0.600 Ref		0.600 Ref	
H	5.950	6.200	0.235	0.244
L1	0.100	0.180	0.004	0.007
K	3.200 Ref		3.200 Ref	

10 Attentions

- Jiangsu Donghai Semiconductor Technology CO.,LTD. reserves the right to change the specification without prior notice! The customer should obtain the latest version of the information before making the order and verify that the information is complete and up to date.
- It is the responsibility of the purchaser for any failure or failure of any semiconductor product under certain conditions. It is the responsibility of the purchaser to comply with safety standards and to take safety measures in the system design and machine manufacturing of Donghai products in order to avoid potential risk of failure. Injury or property damage.
- Product promotion is endless, our company will be dedicated to provide customers with better products.

11 Appendix

Revision history:

Date	REV.	Description	Page
2021.08.19	2.0	Original	9
2023.07.05	3.0	Update	9