

7A 1700V N-channel SiC Power MOSFET

1 Description

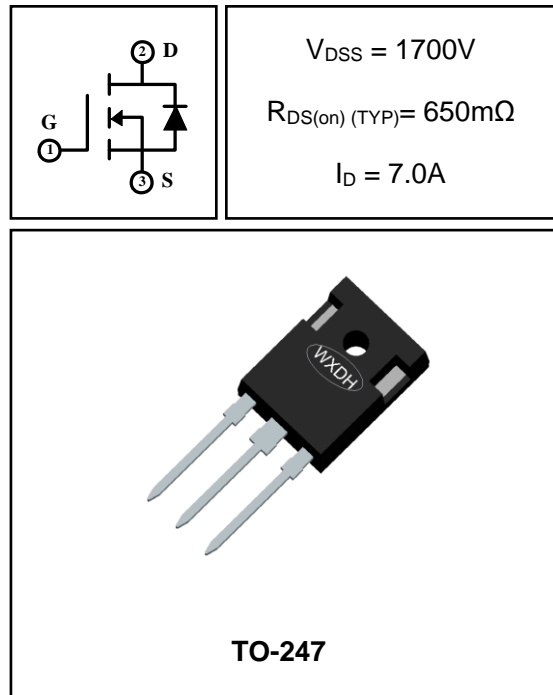
This product family offers state of the art performance. It is designed for high frequency applications where high efficiency and high reliability are required.

2 Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitance
- Easy to Parallel and Simple to Drive
- Ultra-low Drain-gate capacitance

3 Applications

- Auxiliary Power Supplies
- Switch Mode Power Supplies
- High-voltage Capacitive



4 Electrical Characteristics

4.1 Absolute Maximum Rating ($T_c=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Value	Units
Drain-to-Source Voltage	V_{DSmax}	$V_{GS}=0V, I_D=100\mu A$	1700	V
Gate-to-Source Voltage	V_{GSmax}	Absolute maximum values	-10/+25	V
Gate-to-Source Voltage	V_{GSop}	Recommended operational values	-5/+20	V
Continuous Drain Current	I_D	$V_{GS}=20V, T_C=25^\circ C$	7.0	A
		$V_{GS}=20V, T_C=100^\circ C$	4.5	A
Pulsed Drain Current ⁽¹⁾	$I_{D(PULSE)}$	Pulse width t_p limited by T_{Jmax}	9.0	A
Power Dissipation	P_D	$T_c=25^\circ C, T_J=150^\circ C$	62	W
Junction Temperature Range	T_j		-55 ~ 150	$^\circ C$
Storage Temperature Range	T_{stg}		-55 ~ 150	$^\circ C$

4.2 Thermal Characteristics

Parameter	Symbol	Rating	Unit
Thermal Resistance, Junction to Case-sink	R_{thJC}	1.8	$^\circ C/W$
Thermal Resistance, Junction to Ambient	R_{thJA}	40	$^\circ C/W$

4.3 Electrical Characteristics (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Value			Units
			Min	Typ	Max	
Drain-to-Source Breakdown Voltage	BV _{DSS}	I _D =100μA, V _{GS} =0V	1700	---	---	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =1700V, V _{GS} =0V	---	1	100	μA
Gate-to-Source Leakage Current	I _{GSS+}	V _{DS} =0V, V _{GS} =25V	---	10	250	nA
	I _{GSS-}	V _{DS} =0V, V _{GS} =-10V	---	10	250	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =1.0mA	2.0	2.6	4.0	V
		V _{DS} =V _{GS} , I _D =1.0mA, T _J =150°C	---	1.8	---	
Drain-to-Source on-state Resistance	R _{DS(on)}	V _{GS} =20V, I _D =2.0A	---	650	850	mΩ
		V _{GS} =20V, I _D =2.0A, T _J =150°C	---	1300	---	
Transconductance	g _{fs}	V _{DS} =20 V, I _D =2.0 A	---	1.06	---	S
		V _{DS} =20V, I _D =2.0A, T _J =150°C	---	1.14	---	
Input Capacitance	C _{iSS}	V _{GS} =0V, V _{DS} =1000V, f=1.0MHz, VAC=25mV	---	194	---	pF
Output Capacitance	C _{oss}		---	13	---	
Reverse Transfer Capacitance	C _{rSS}		---	1.8	---	
Coss Stored Energy	E _{oss}		---	6.6	---	
Turn-On Switching Energy	E _{ON}	V _{DS} =1200V, V _{GS} =-5V/20V I _D =2.0A, R _{G(ext)} =2.5Ω, L=100μH	---	5	---	mJ
Turn-Off Switching Energy	E _{OFF}		---	9.2	---	
Turn-on Delay Time	t _{d(on)}	V _{DS} =1200V, V _{GS} =-5V/20V, I _D =2.0A R _{G(ext)} =2.5Ω, R _L =20Ω	---	13.8	---	ns
Turn-on Rise Time	t _r		---	22.8	---	
Turn-off Delay Time	t _{d(off)}		---	38	---	
Turn-off Fall Time	t _f		---	14	---	
Internal Gate Resistance	R _{G(int)}	f=1MHz, V _{AC} =25mV	---	18	---	Ω
Total Gate Charge	Q _g	V _{DS} =1200V, V _{GS} =-5V/20V, I _D =2.0A	---	23	---	nC
Gate-to-Source Charge	Q _{gs}		---	5.4	---	
Gate-to-Drain("Miller") Charge	Q _{gd}		---	7.6	---	
Drain-Source Diode Characteristics						
Diode Forward Voltage ⁽³⁾	V _{SD}	V _{GS} =-5V, I _{SD} =25A	---	4.2	---	V
		V _{GS} =-5V, I _{SD} =25A, T _J =150°C	---	3.9	---	
Diode Forward Current	I _S	T _C =25°C	---	---	7.0	A
Reverse Recovery Time ⁽³⁾	t _{rr}	V _R =1200V, I _{SD} =2.0A	---	25	---	ns
Reverse Recovery Charge ⁽³⁾	Q _{rr}		---	15	---	nC
Peak Reverse Recovery Current	I _{rrm}		---	2.8	---	A

Notes:

- 1: Repetitive rating, pulse width limited by maximum junction temperature.
- 2: Surface mounted on FR4 Board, t_s≤10sec.
- 3: Pulse width ≤ 300μs, duty cycle ≤ 2%.

5 Typical characteristics diagrams

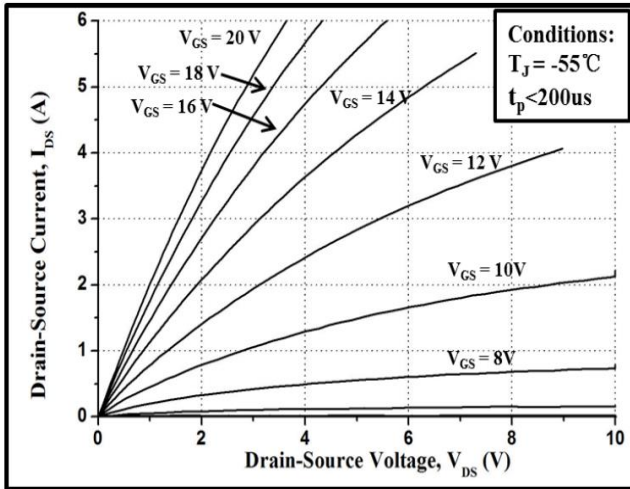


Figure 1. Output Characteristics $T_J = -55^\circ\text{C}$

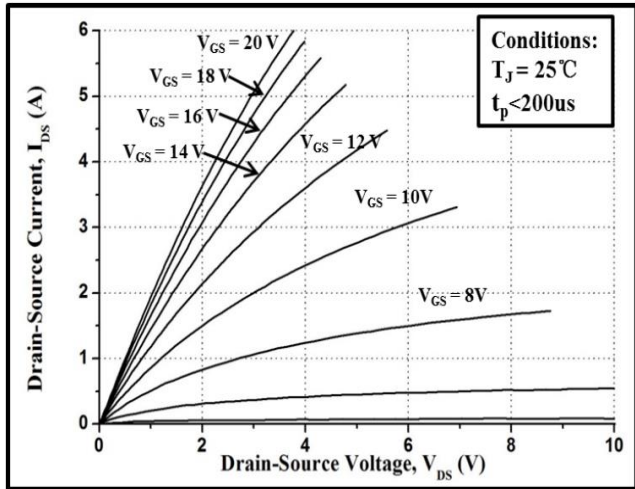


Figure 2. Output Characteristics $T_J = 25^\circ\text{C}$

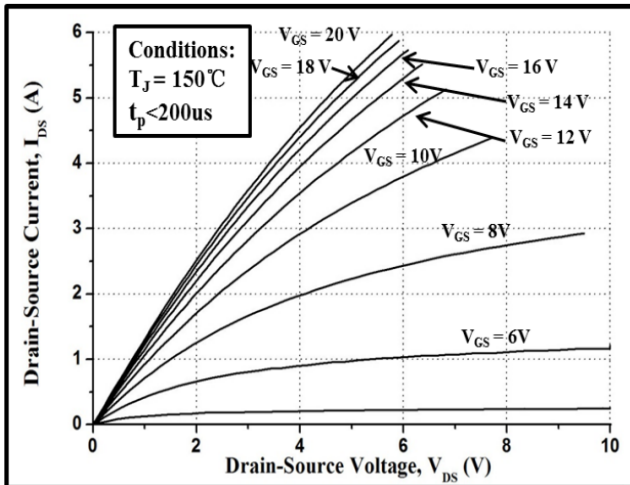


Figure 3. Output Characteristics $T_J = 150^\circ\text{C}$

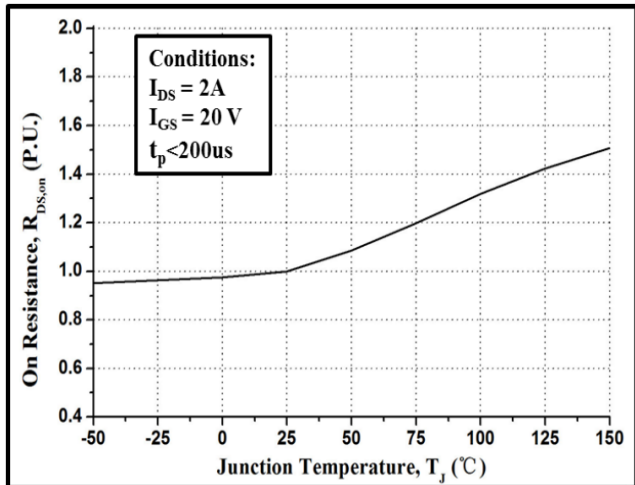


Figure 4. Normalized On-Resistance vs. Temperature

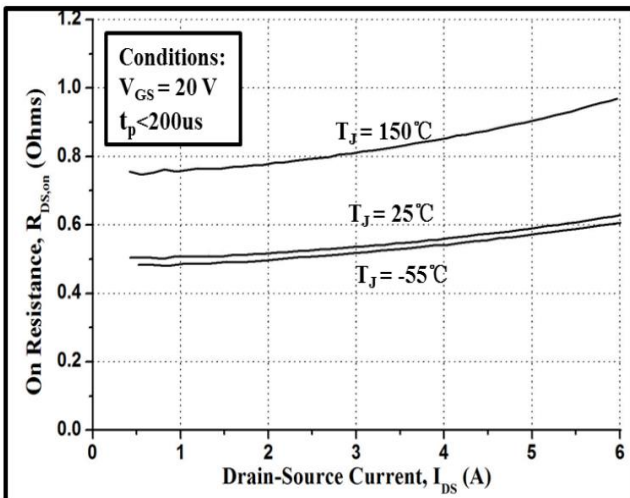


Figure 5. On-Resistance vs. Drain Current
For Various Temperatures

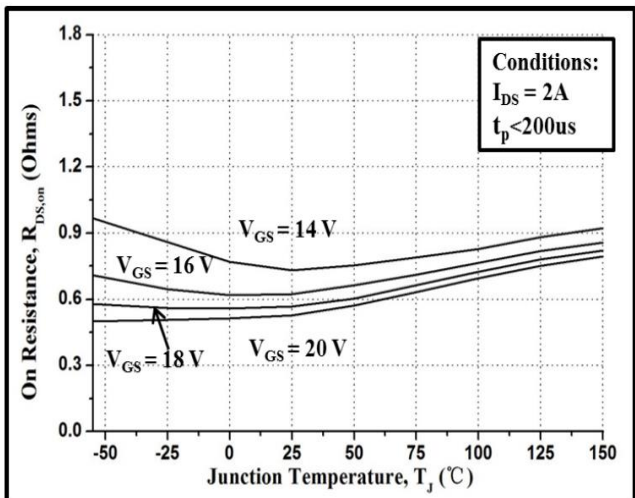


Figure 6. On-Resistance vs. Temperature
For Various Gate Voltage

5 Typical characteristics diagrams(continues)

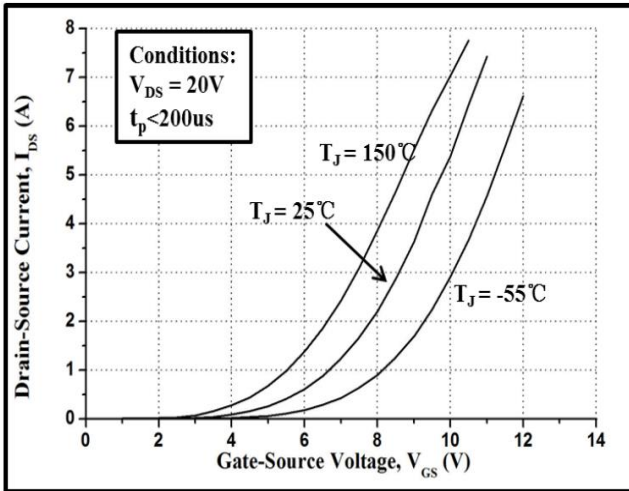


Figure 7. Transfer Characteristic for Various Junction Temperatures

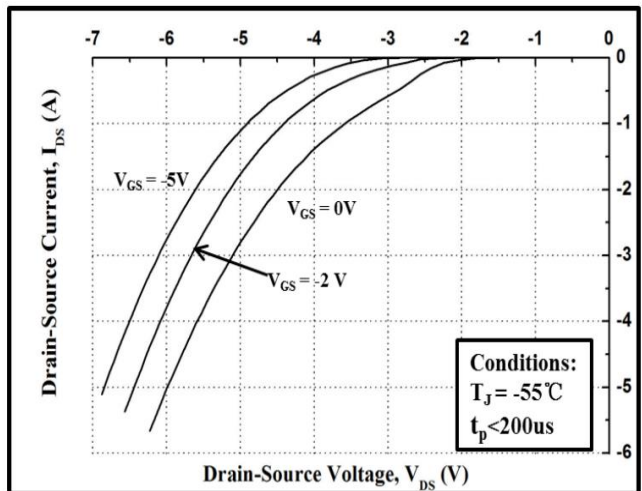


Figure 8. Body Diode Characteristic at -55 °C

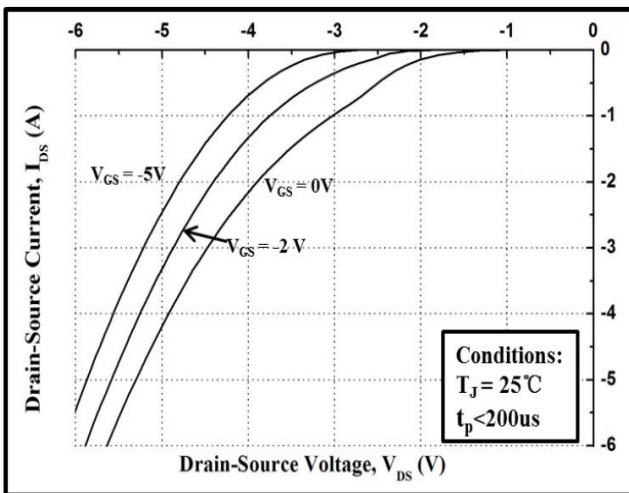


Figure 9. Body Diode Characteristic at 25 °C

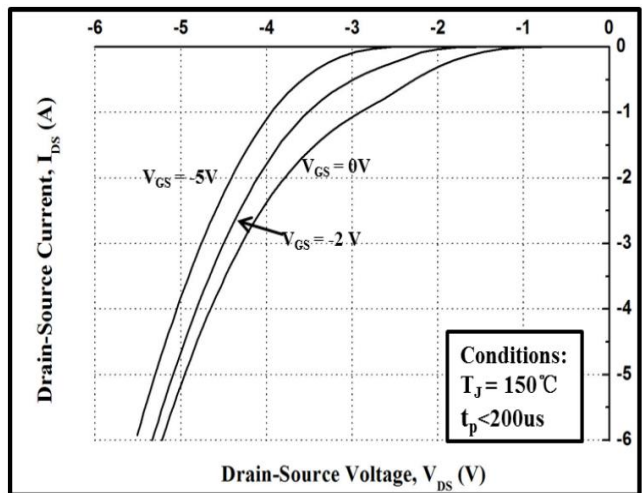


Figure 10. Body Diode Characteristic at 150 °C

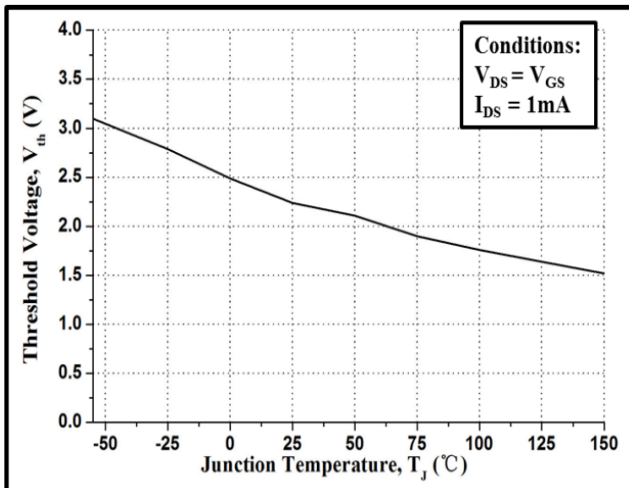


Figure 11. Threshold Voltage vs. Temperature

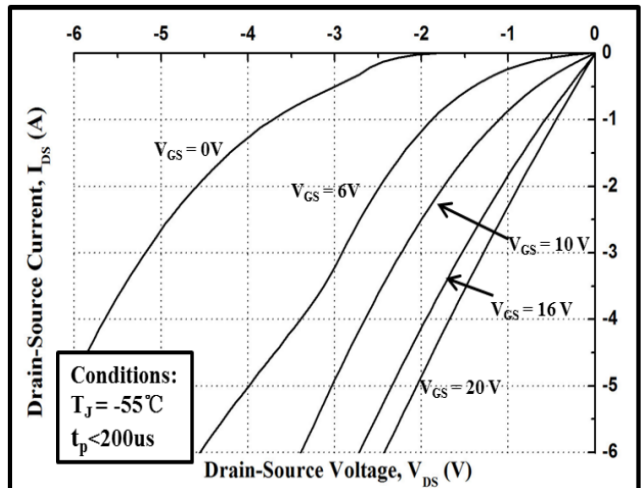


Figure 12. 3rd Quadrant Characteristic at -55 °C

5 Typical characteristics diagrams(continues)

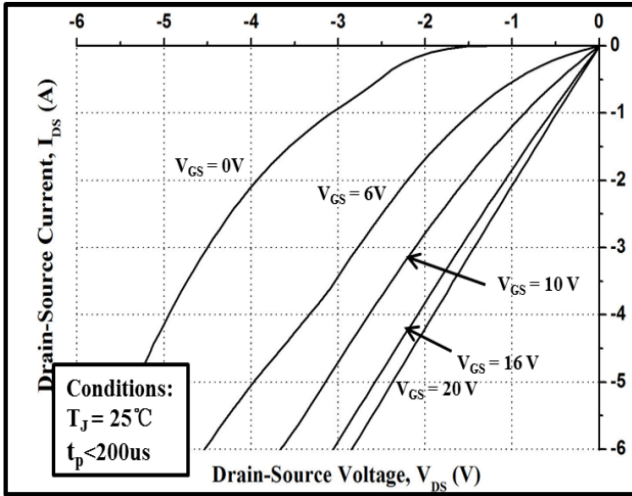


Figure 13. 3rd Quadrant Characteristic at 25 °C

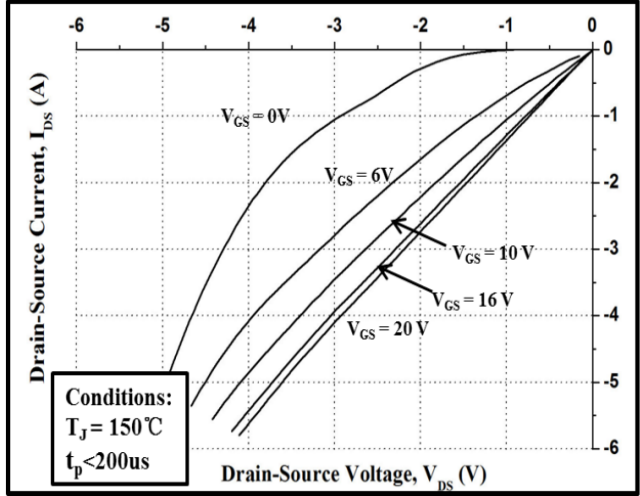


Figure 14. 3rd Quadrant Characteristic at 150 °C

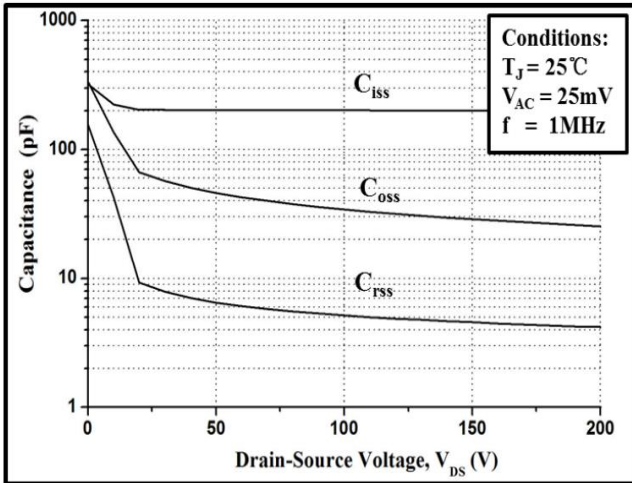


Figure 15. Capacitances vs. Drain-Source Voltage (0 - 200V)

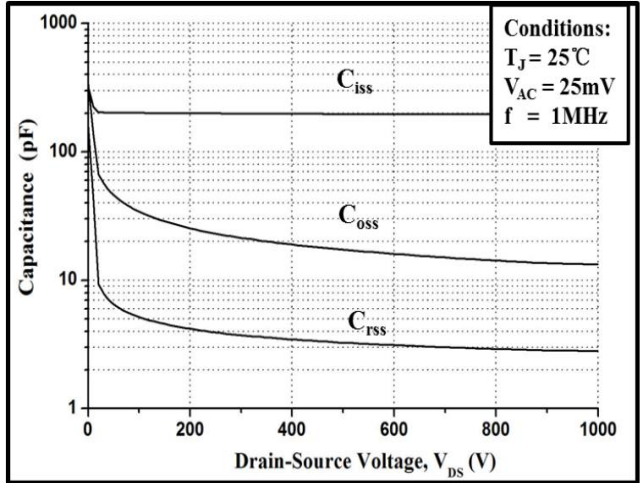


Figure 16. Capacitances vs. Drain-Source Voltage (0 - 1000V)

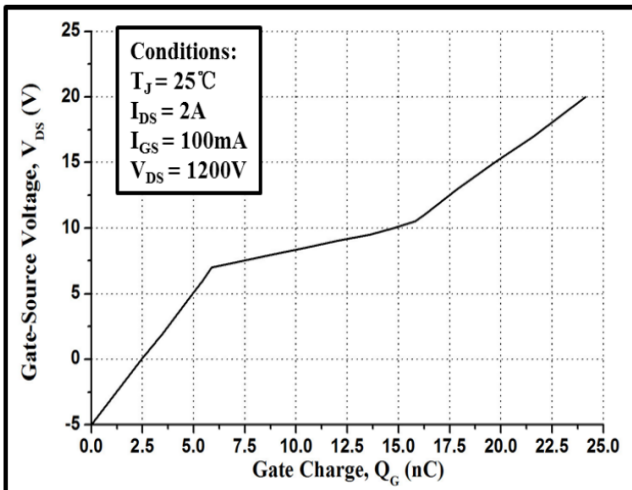


Figure 17. Gate Charge Characteristic

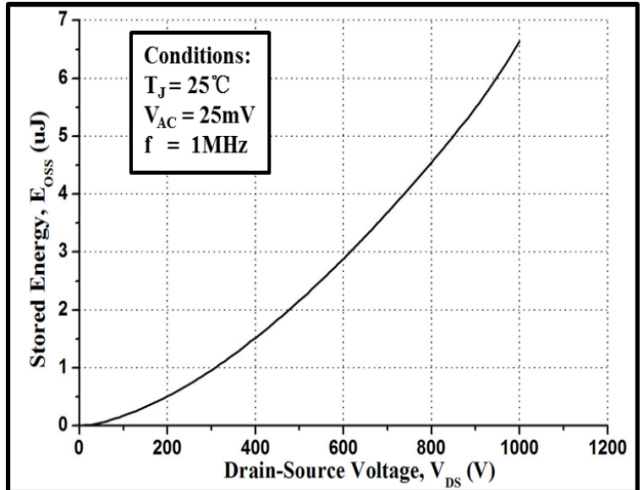


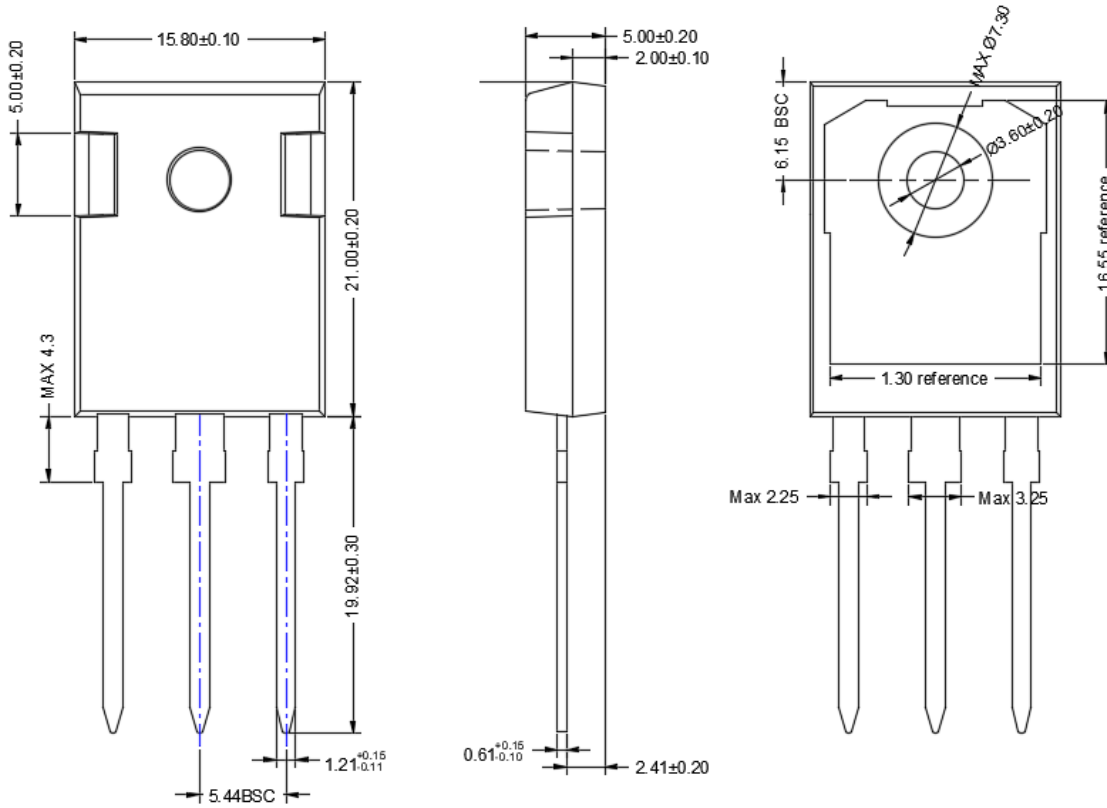
Figure 18. Output Capacitor Stored Energy

6 Product Specifications and Packaging Models

Product Model	Package Type	Mark Name	RoHS	Package	Quantity
DCC650M170G1	TO-247	DCC650M170G1	Pb-free	Tube	300/box

7 Dimensions

TO-247-3L PACK OUTLINE DIMENSIONS



8 Attentions

- Jiangsu Donghai Semiconductor Technology CO.,LTD. reserves the right to change the specification without prior notice! The customer should obtain the latest version of the information before making the order and verify that the information is complete and up to date.
- It is the responsibility of the purchaser for any failure or failure of any semiconductor product under certain conditions. It is the responsibility of the purchaser to comply with safety standards and to take safety measures in the system design and machine manufacturing of Donghai products in order to avoid potential risk of failure. Injury or property damage.
- Product promotion is endless, our company will be dedicated to provide customers with better products.

9 Appendix

Revision history:

Date	REV.	Description	Page
2021.05.20	1.0	Original	7