

Features

- AEC-Q101 qualified
- Low on resistance
- Low reverse transfer capacitances
- 100% single pulse avalanche energy test
- 100% ΔVDS test
- Pb-Free plating / Halogen-Free / RoHS compliant

Key Parameters

V_{DS}	100V
$R_{DS(on)}(typ.)$	2.2mΩ
V_{TH}	3V
$I_D(Silicon\ limit)$	240A
$I_D(Package\ limit)$	180A
$C_{iss}@10V$	7035pF
Q_{gd}	27nC

Applications

- Power switching applications
- DC-DC converters
- Full bridge control
- Automotive applications



AEC Qualified



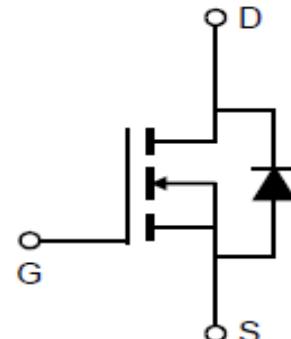
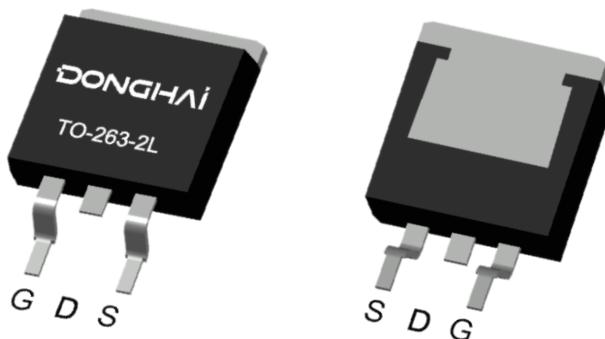
LEAD FREE



Halogen FREE



TO-263



Marking & Packing Information

Part #	Package	Marking	Tube/Reel	Qty(pcs)
DSE026N10N3A	TO-263	DSE026N10N3A	Reel	800/box

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	100	V
Gate-Source voltage	V _{GS}	±20	V
Continuous drain current T _C = 25°C (Silicon limit) T _C = 25°C (Package limit) T _C = 100°C	I _D	240 180 170	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _D pulse	720	A
Avalanche energy, single pulse (L=0.5mH, R _g =25Ω)	E _{AS}	1225	mJ
Power dissipation T _C = 25°C T _A = 25°C	P _{tot}	300 2.3	W
Operating junction and storage temperature	T _j , T _{stg}	-55...+175	°C

Thermal Resistance

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R _{thJC}	0.50	°C/W
Thermal resistance, junction – ambient(min. footprint)	R _{thJA}	65	

Electrical Characteristic (at T_j = 25 °C, unless otherwise specified)

Static Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Drain-source breakdown voltage	BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250uA
Gate threshold voltage	V _{GS(th)}	2.0	3.0	4.0	V	V _{DS} =V _{GS} , I _D =250uA
Zero gate voltage drain current	I _{DSS}	-	-	1	μA	V _{DS} =100V, V _{GS} =0V
		-	-	100		T _j =25°C T _j =125°C
Gate-source leakage current	I _{GSS}	-	-	100	nA	V _{GS} =20V, V _{DS} =0V
Drain-source on-state resistance	R _{DS(on)}		2.2	2.6	mΩ	V _{GS} =10V, I _D =90A, T _j =25°C
Transconductance	g _{fs}	-	186	-	S	V _{DS} =5V, I _D =90A

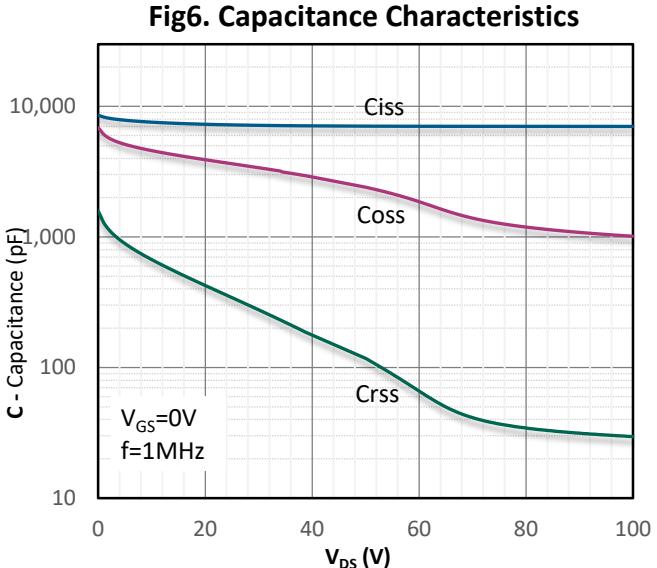
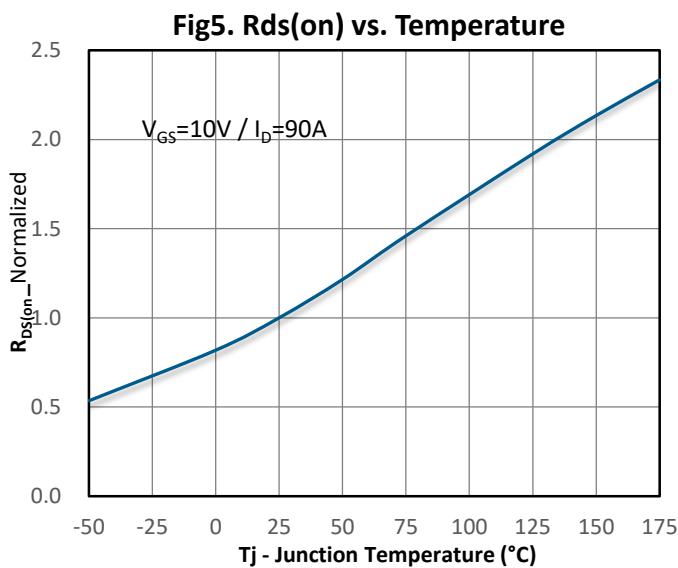
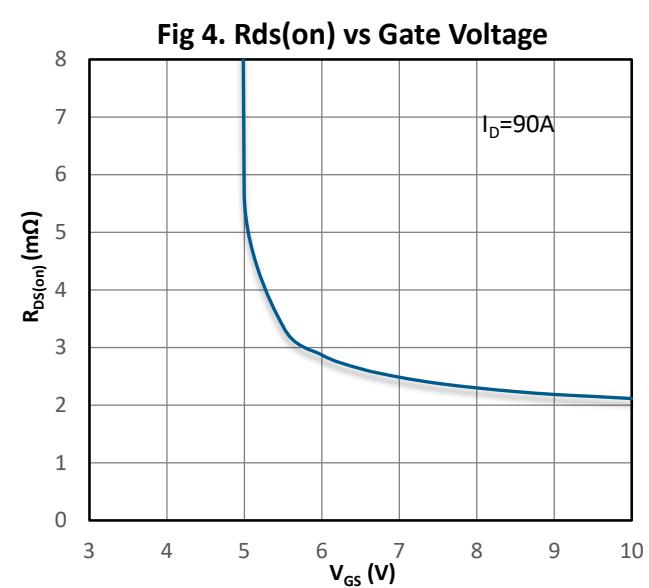
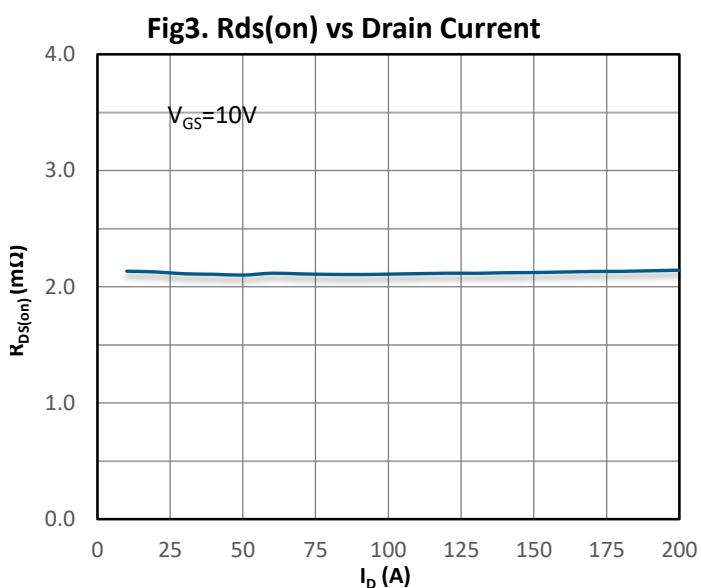
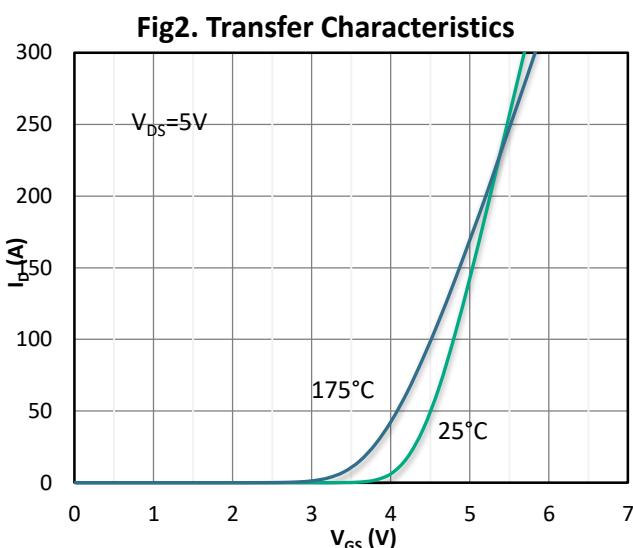
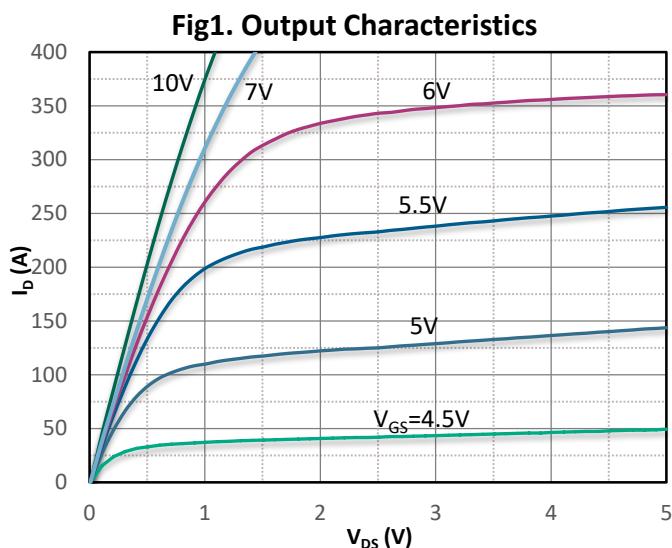
Dynamic Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Input Capacitance	C _{iss}	-	7035	-	pF	V _{GS} =0V, V _{DS} =50V, f=1MHz
Output Capacitance	C _{oss}	-	2398	-		
Reverse Transfer Capacitance	C _{rss}	-	117	-		
Gate Total Charge	Q _G	-	107	-	nC	V _{GS} =10V, V _{DS} =50V, I _D =90A
Gate-Source charge	Q _{gs}	-	37	-		
Gate-Drain charge	Q _{gd}	-	27	-		
Gate plateau voltage	V _{plateau}	-	5.6	-	V	
Turn-on delay time	t _{d(on)}	-	28	-	ns	V _{GS} =10V, V _{DD} =50V, ID=90A, R _{G_ext} =3Ω
Rise time	t _r	-	108	-		
Turn-off delay time	t _{d(off)}	-	56	-		
Fall time	t _f	-	124	-		
Gate resistance	R _G	-	1.8	-	Ω	V _{GS} =0V, V _{DS} =0V, f=1MHz

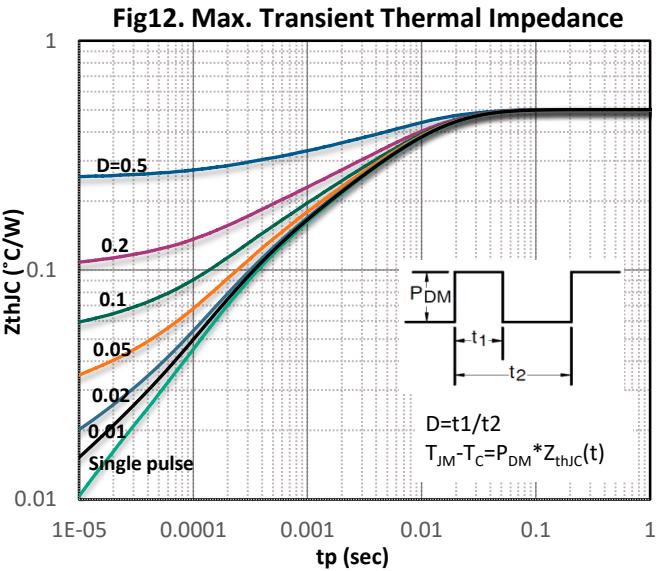
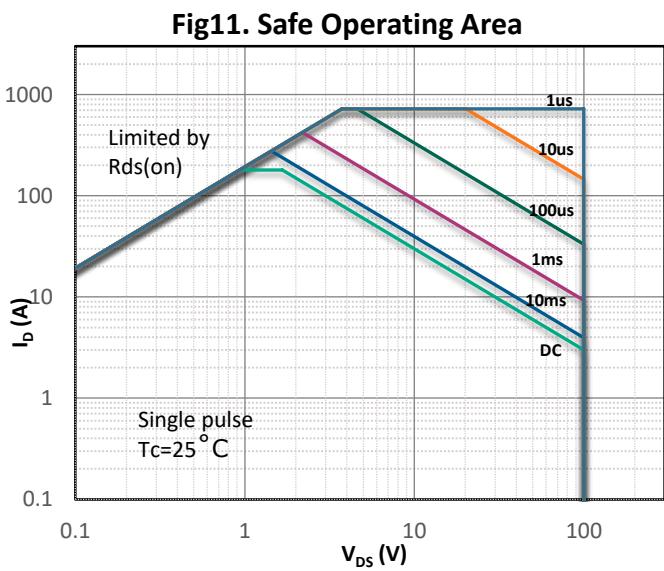
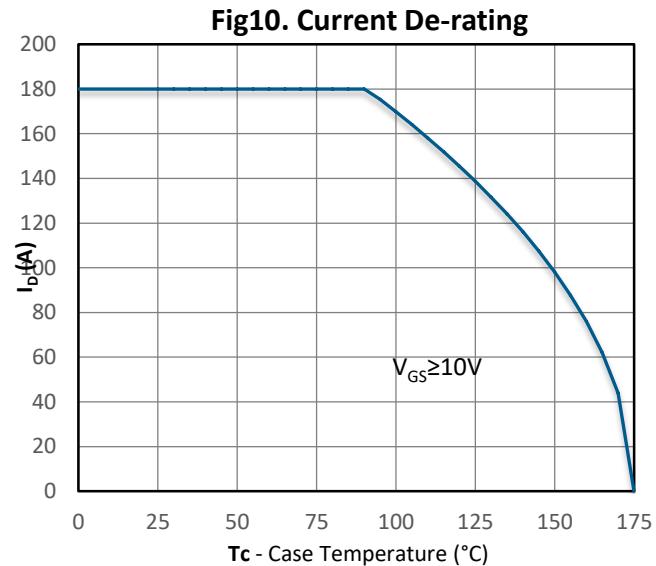
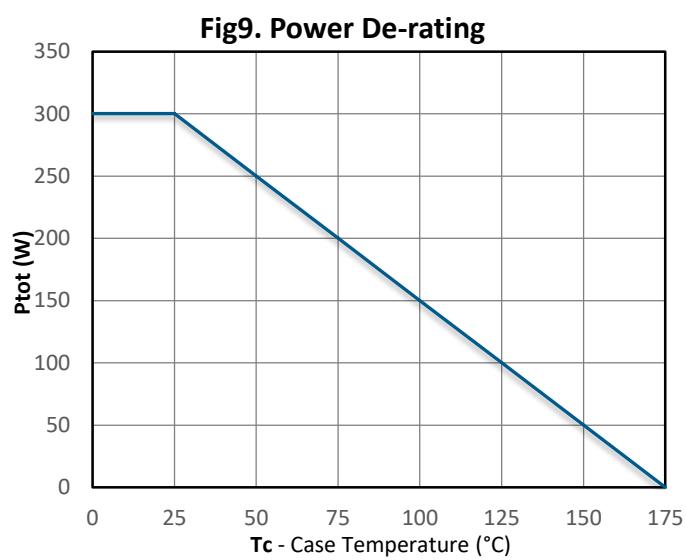
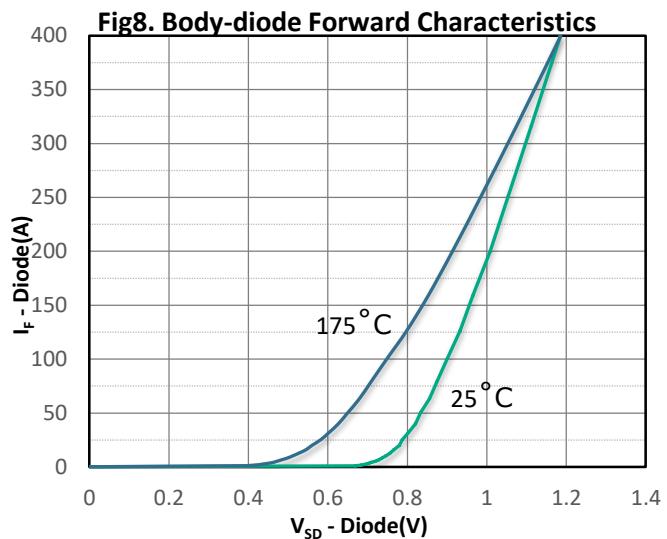
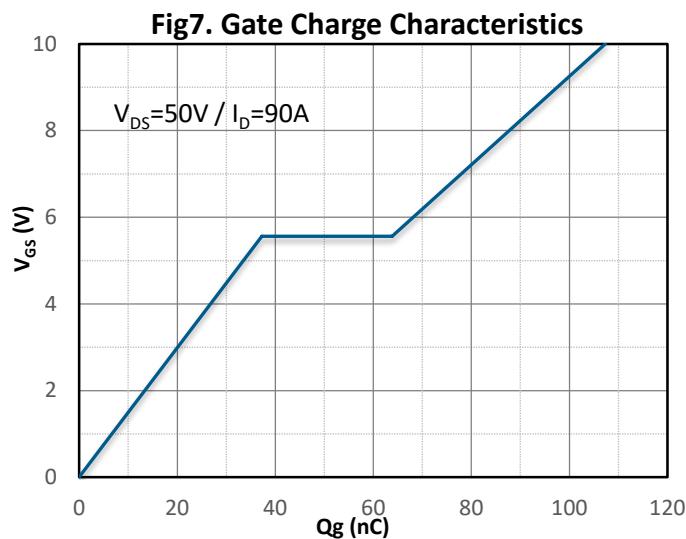
Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Diode Max Current	I _S		-	180	A	-
Diode Forward Voltage	V _{SD}	-	-	1.2	V	V _{GS} =0V, I _{SD} =90A
Diode Reverse Recovery Time	t _{rr}	-	107	-	ns	I _F =90A, dI/dt=100A/μs
Diode Reverse Recovery Charge	Q _{rr}	-	143	-		

Typical Characteristics Diagram

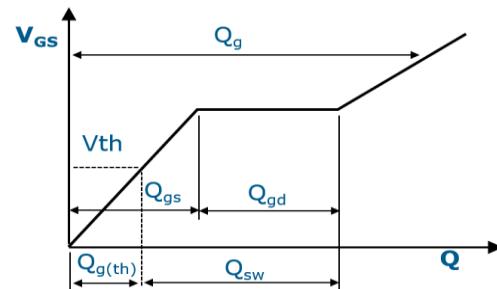
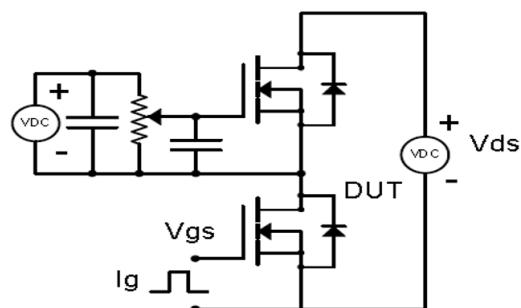


Typical Characteristics Diagram

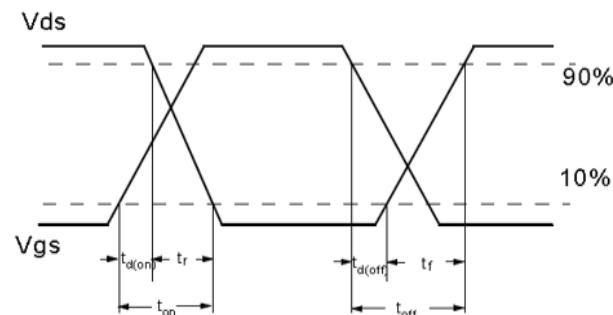
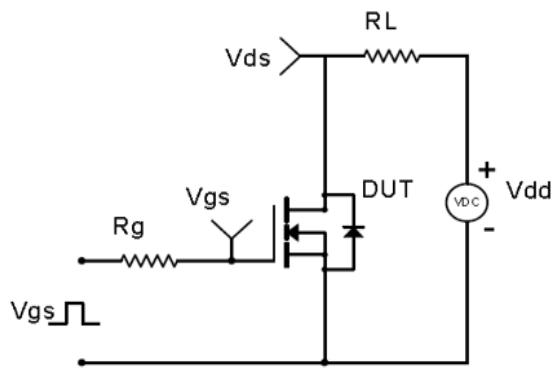


Test Circuit & Waveform

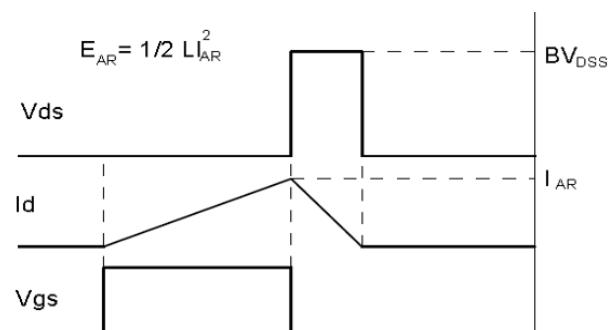
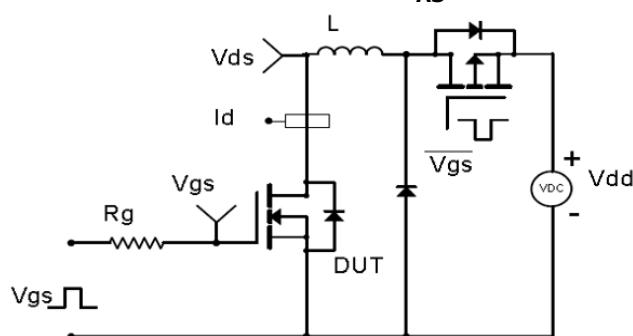
Gate Charge Test Circuit & Waveform



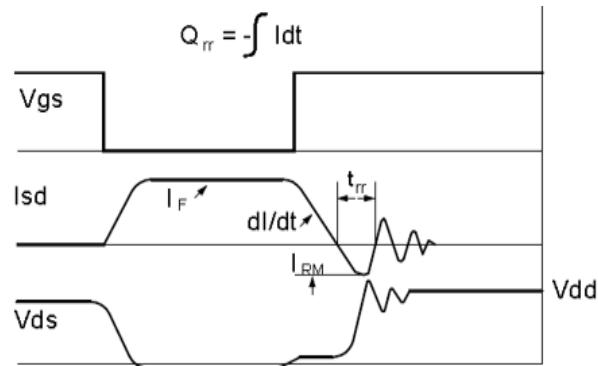
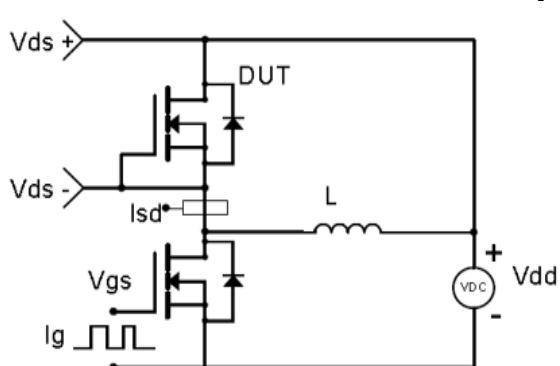
MOSFET Switching Test Circuit & Waveform



E_{AS} Test Circuit & Waveform

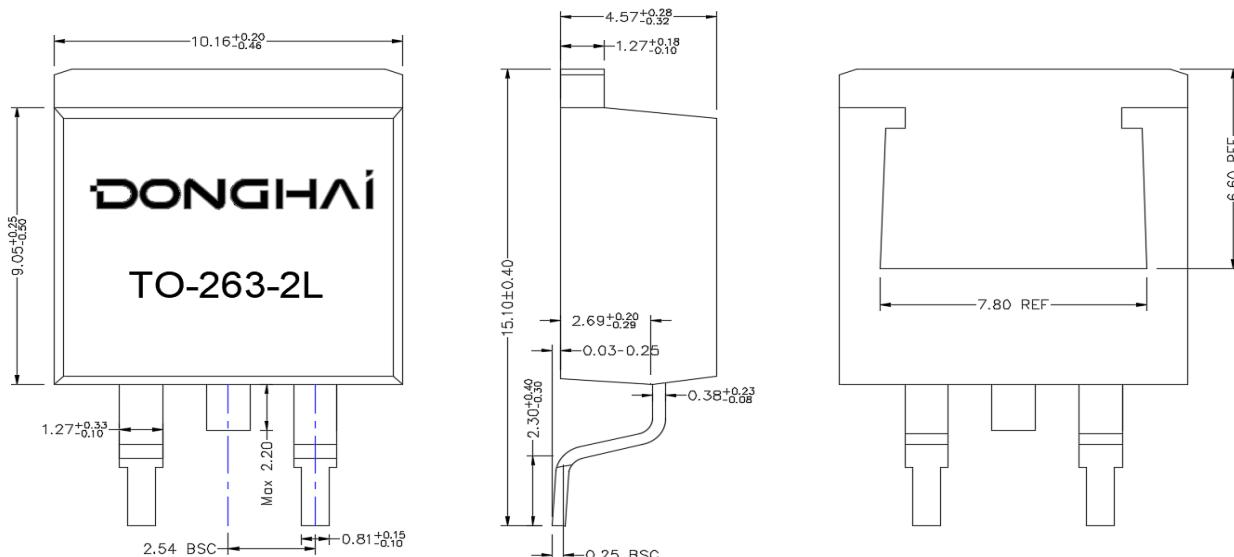


Diode Recovery Test Circuit & Waveform



Package Outline : TO-263

*Dimensions in mm



Revision History

Revison	Date	Major changes
1.0	2023/8/18	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as aviation, aerospace, life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are responsible for providing adequate safe measures when design their systems.

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