

Features

- **AEC-Q101 qualified**
- Extremely low on-resistance $R_{DS(on)}$
- 175°C operating temperature
- Pb-Free plating / Halogen-Free / RoHS compliant
- 100% avalanche screened
- 100% ΔV_{DS} test

Applications

- Motor Control and Drive
- Charge/Discharge for Battery Management System
- Synchronous Rectifier for SMPS
- Automotive applications

Key Parameters

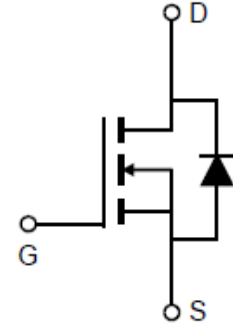
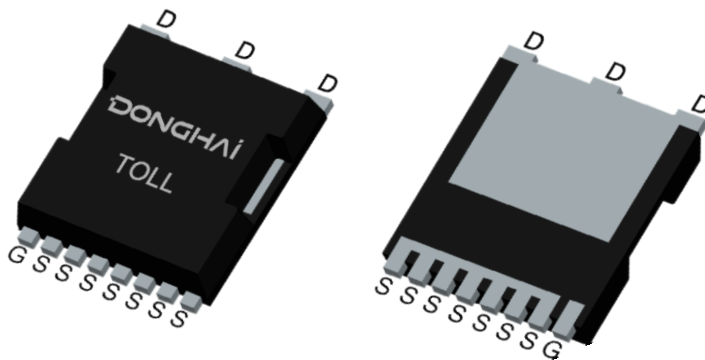
V_{DS}	85V
$R_{DS(on)typ.}$	0.9mΩ
I_D (Silicon limit)	433A
I_D (Package limit)	360A
V_{th}	3V
$C_{iss@10V}$	16105pF
Q_{gd}	37nC



AEC Qualified



TOLL



Marking & Packing Information

Part #	Package	Marking	Tube/Reel	Qty(pcs)
DSU011N08N3A	TOLL	DSU011N08N3A	Tape & Reel	800/box

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	85	V
Gate-Source voltage	V_{GS}	±20	V
Continuous drain current $T_C = 25^\circ\text{C}$ (Silicon limit) $T_C = 25^\circ\text{C}$ (Package limit) $T_C = 100^\circ\text{C}$	I_D	433 360 306	A
Pulsed drain current ($T_C = 25^\circ\text{C}$, t_p limited by T_{jmax})	$I_{D\ pulse}$	1440	A
Avalanche energy, single pulse ($L=0.5\text{mH}$, $R_g=25\Omega$) ^[1]	E_{AS}	2601	mJ
Power dissipation	P_{tot}	$T_C = 25^\circ\text{C}$	429 W
		$T_A = 25^\circ\text{C}$	2.3 W
Operating junction and storage temperature	T_j, T_{stg}	-55...+175	°C

Notes: 1. EAS was tested at $T_j = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $I_d=71\text{A}$.

Thermal Resistance

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	0.35	°C/W
Thermal resistance, junction – ambient(min. footprint)	R_{thJA}	65	

Electrical Characteristic (at $T_j = 25^\circ\text{C}$, unless otherwise specified)

Static Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Drain-source breakdown voltage	BV_{DSS}	85	-	-	V	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.0	3.0	4.0	V	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$
Zero gate voltage drain current	I_{DSS}	-	-	1	μA	$V_{DS}=80\text{V}$, $V_{GS}=0\text{V}$ $T_j=25^\circ\text{C}$
		-	-	100		$T_j=125^\circ\text{C}$
Gate-source leakage current	I_{GSS}	-	-	100	nA	$V_{GS}=20\text{V}$, $V_{DS}=0\text{V}$
Drain-source on-state resistance	$R_{DS(on)}$	-	0.9	1.1	mΩ	$V_{GS}=10\text{V}$, $I_D=100\text{A}$
Transconductance	g_{fs}	-	240	-	S	$V_{DS}=5\text{V}$, $I_D=100\text{A}$

Dynamic Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Input Capacitance	C_{iss}	-	16105	-	pF	$V_{GS}=0V, V_{DS}=40V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	3102	-		
Reverse Transfer Capacitance	C_{rss}	-	64	-		
Gate Total Charge	Q_G	-	210	-	nC	$V_{GS}=10V, V_{DS}=40V,$ $I_D=60A$
Gate-Source charge	Q_{gs}	-	71	-		
Gate-Drain charge	Q_{gd}	-	37	-		
Gate plateau voltage	$V_{plateau}$	-	4.8	-	V	
Turn-on delay time	$t_{d(on)}$	-	39	-	ns	$V_{GS}=10V, V_{DD}=40V,$ $R_{G_ext}=3\Omega, I_D=50A$
Rise time	t_r	-	78	-		
Turn-off delay time	$t_{d(off)}$	-	154	-		
Fall time	t_f	-	91	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Diode Max Current	I_S		-	360	A	-
Diode Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0V, I_{SD}=100A$
Diode Reverse Recovery Time	t_{rr}	-	146	-	ns	$I_F=50A, dI/dt=100A/\mu s$
Diode Reverse Recovery Charge	Q_{rr}	-	198	-	nC	

Typical Characteristics Diagram

Fig1. Output Characteristics

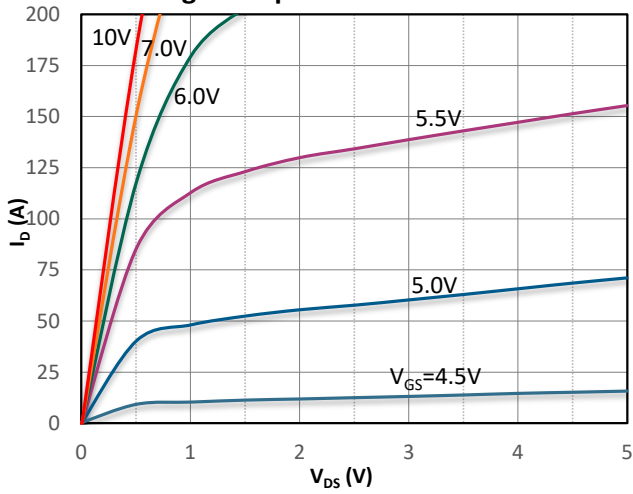


Fig2. Transfer Characteristics

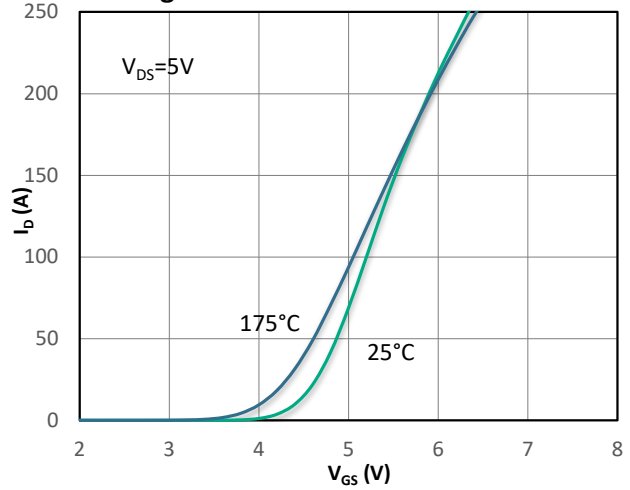


Fig3. R_{DS(on)} vs Drain Current

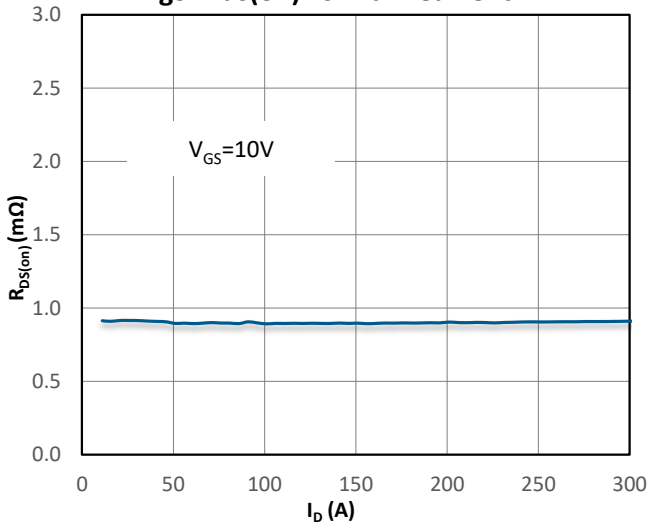


Fig 4. R_{DS(on)} vs Gate Voltage

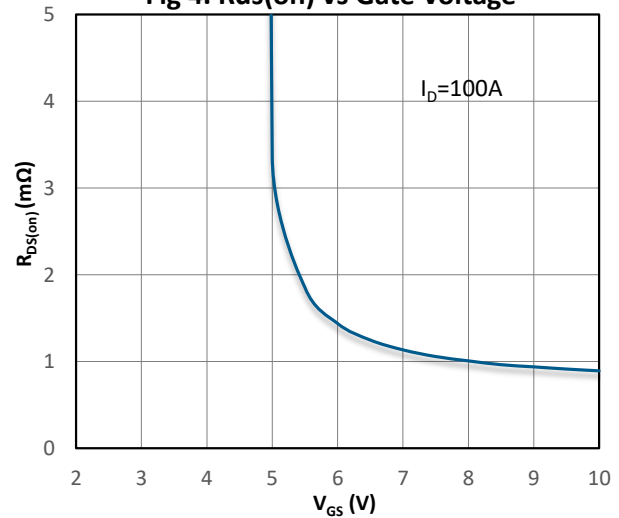


Fig5. R_{DS(on)} vs. Temperature

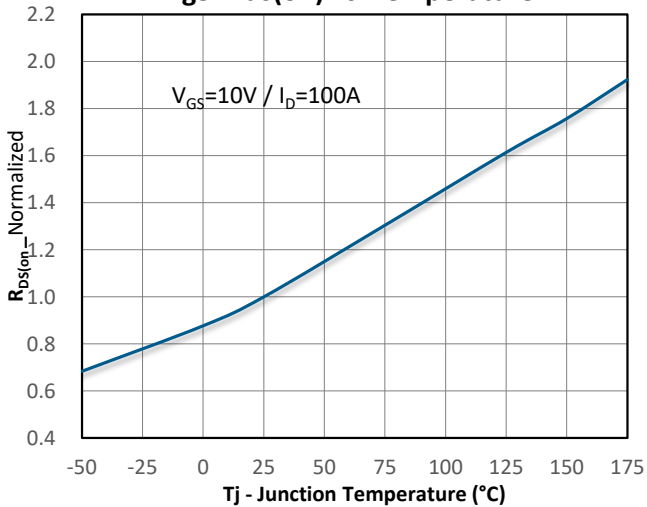
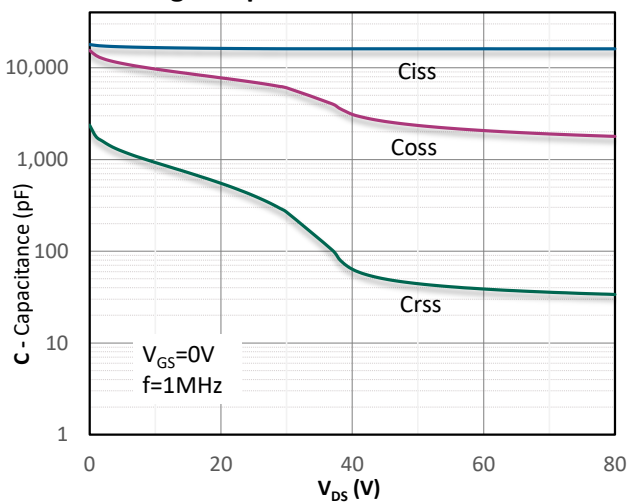
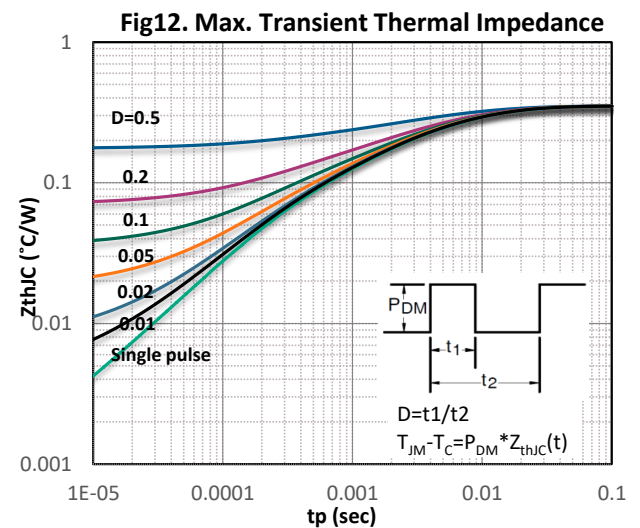
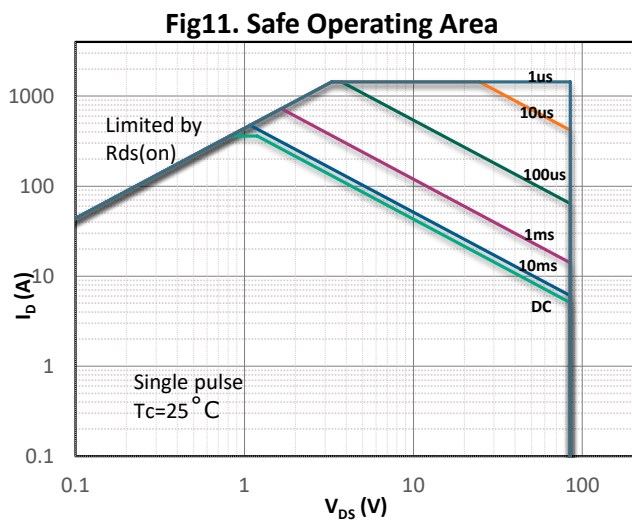
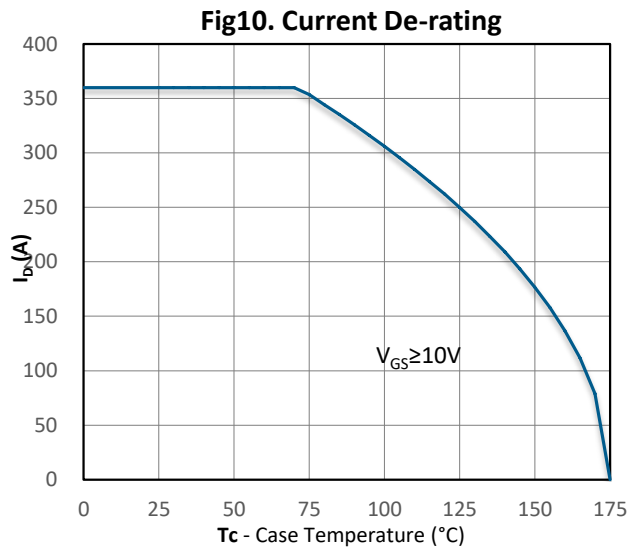
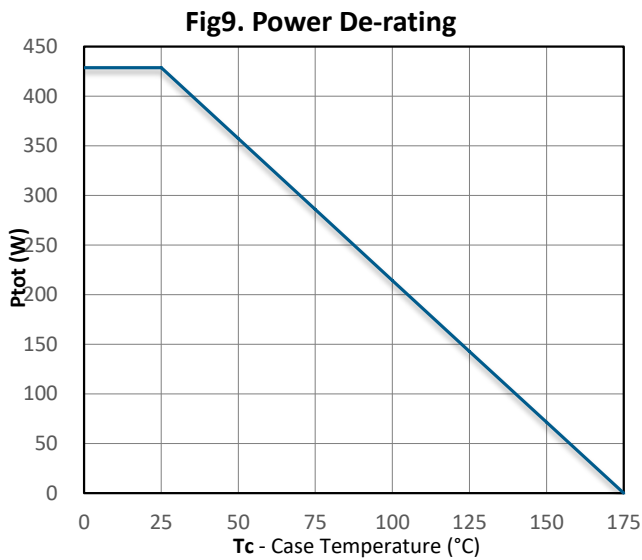
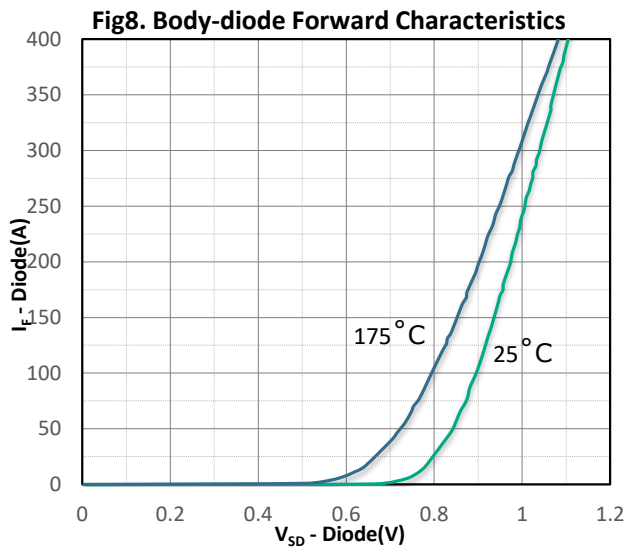
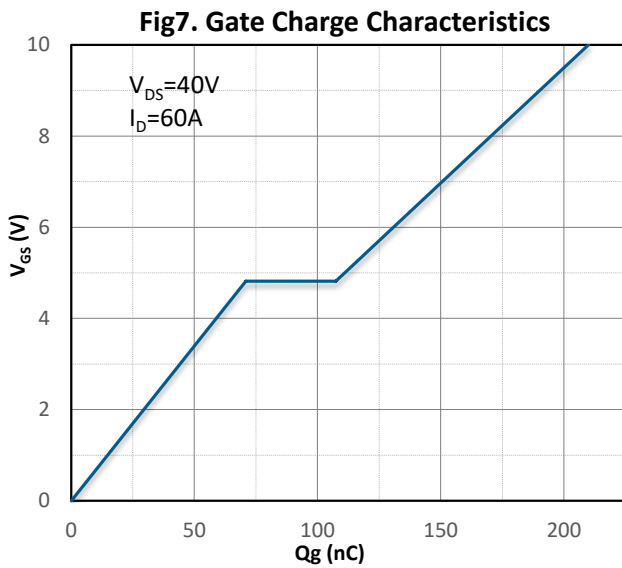


Fig6. Capacitance Characteristics

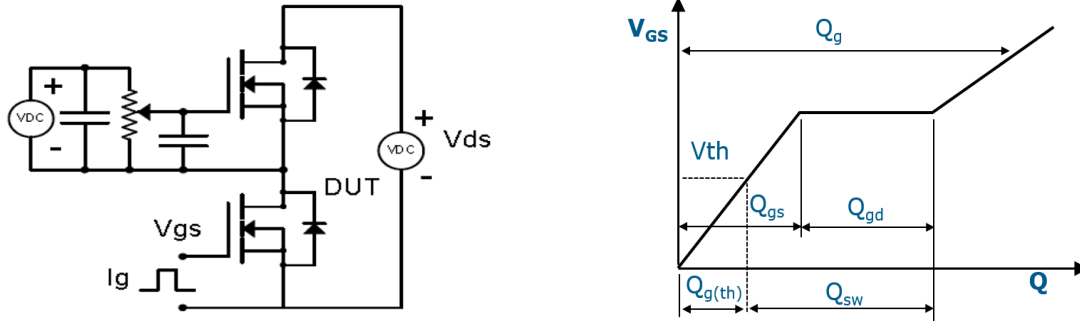


Typical Characteristics Diagram

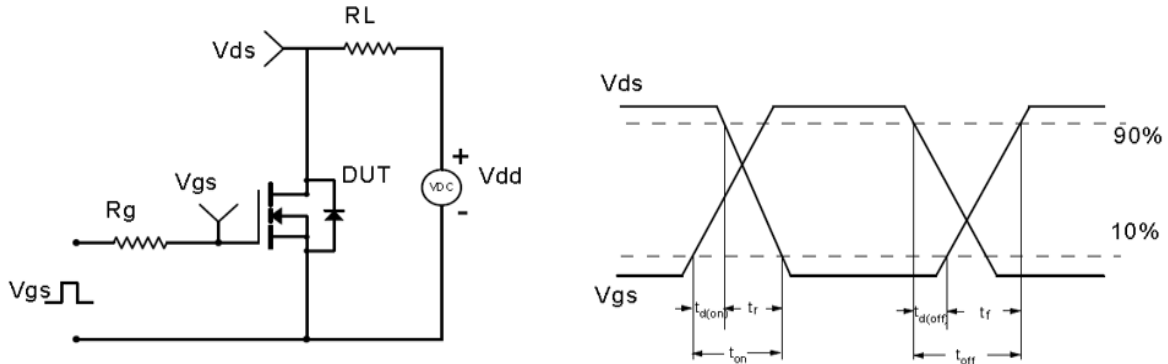


Test Circuit & Waveform

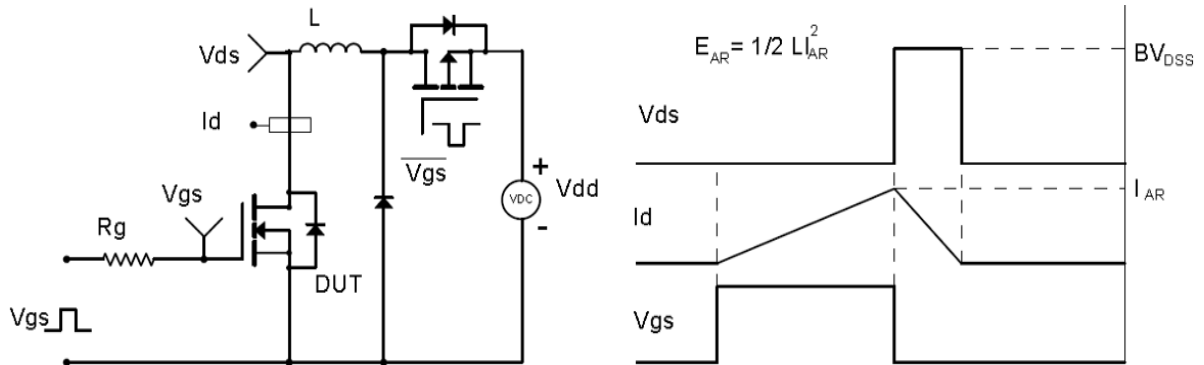
Gate Charge Test Circuit & Waveform



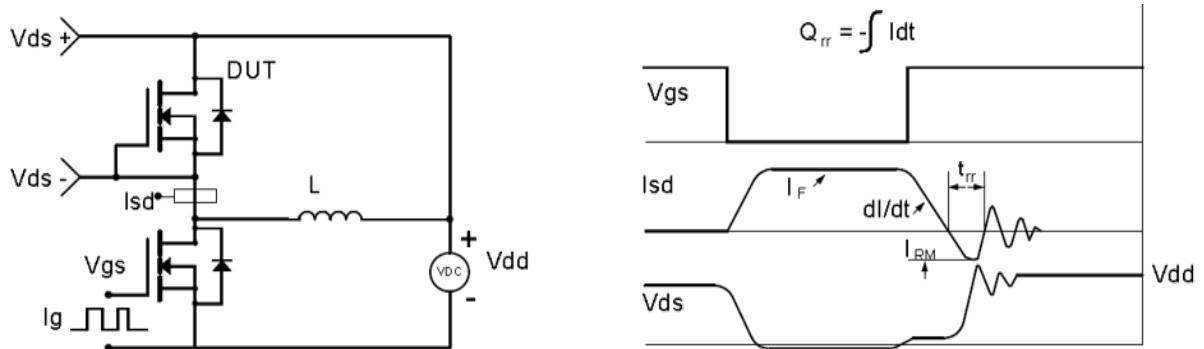
MOSFET Switching Test Circuit & Waveform



E_{AS} Test Circuit & Waveform

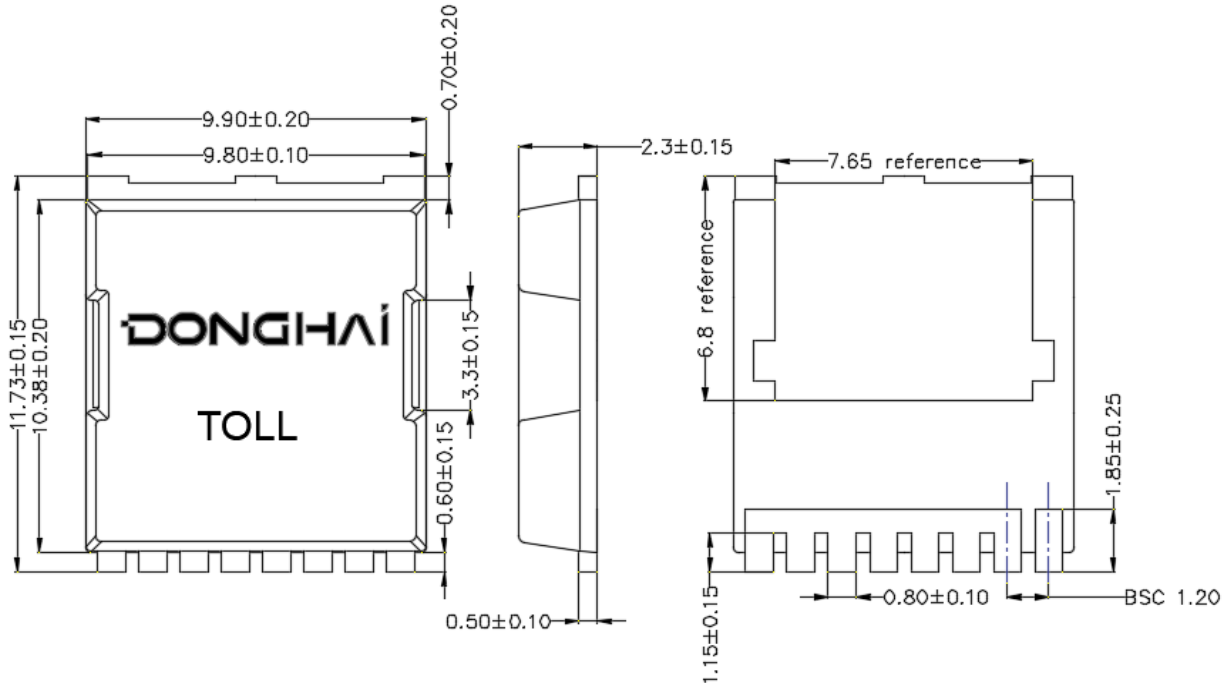


Diode Recovery Test Circuit & Waveform



Package Outline : TOLL

*Dimensions in mm



Revision History

Revison	Date	Major changes
1.0	2023/5/8	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as aviation, aerospace, life-support devices or systems.

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